TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVI)

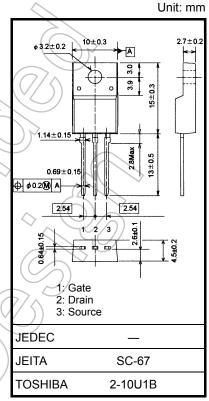
2SK3563

Switching Regulator Applications

- Low drain-source ON resistance: $R_{DS (ON)} = 1.35 \Omega (typ.)$
- High forward transfer admittance: |Y_{fs}| = 3.5 S (typ.)
- Low leakage current: $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 500 \text{ V)}$
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit |
|--|------------------------------|-------------------|---------|--|
| Drain-source voltage | | V_{DSS} | 500 | (X) |
| Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$) | | V_{DGR} | 500 | $\langle \langle \psi \rangle \rangle$ |
| Gate-source voltage | | V_{GSS} | ±30 | V |
| Drain current | DC (Note 1) | I _D | 5 | |
| | Pulse (t = 1 ms) (Note 1) | I _{DP} | 20 | ✓ A |
| Drain power dissipati | on (Tc = 25°C) | P _D | 35 | W |
| Single pulse avalanche energy (Note 2) | | E _{AS} | 180 | mJ |
| Avalanche current | | IAR | 5 | A |
| Repetitive avalanche energy (Note 3) | | EAR | 3.5 | mJ |
| Channel temperature | | ((T _{ch} | 150 | ∕/°C |
| Storage temperature range | | T _{stg} | -55~150 | °C |



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

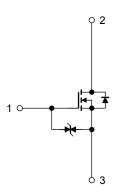
| Characteristics | Symbol | Max | Unit |
|--|------------|------|------|
| Thermal resistance, channel to case | Rth (ch-c) | 3.57 | °C/W |
| Thermal resistance, channel to ambient | Rth (ch-a) | 62.5 | °C/W |

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: VDD = 90 V, Tch = 25°C(initial), L = 12.2 mH, IAR = 5 A, RG = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



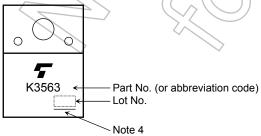
Electrical Characteristics (Ta = 25°C)

| Chara | acteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|------------------------------------|----------------|----------------------|--|----------------|------|------|------|
| Gate leakage cur | rent | I _{GSS} | $V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | _ | ±10 | μΑ |
| Gate-source brea | akdown voltage | V (BR) GSS | $I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$ | ±30 | _ | | V |
| Drain cut-off curre | ent | I _{DSS} | V _{DS} = 500 V, V _{GS} = 0 V | / | _ | 100 | μΑ |
| Drain-source brea | akdown voltage | V (BR) DSS | I _D = 10 mA, V _{GS} = 0 V | 500 | _ | | V |
| Gate threshold vo | oltage | V _{th} | V _{DS} = 10 V, I _D = 1 mA | 2.0 |) >_ | 4.0 | V |
| Drain-source ON | resistance | R _{DS} (ON) | V _{GS} = 10 V, I _D = 2.5 A |) | 1.35 | 1.50 | Ω |
| Forward transfer | admittance | Y _{fs} | V _{DS} = 10 V, I _D = 2.5 A | 1.5 | 3.5 | | S |
| Input capacitance | • | C _{iss} | | | 550 | | |
| Reverse transfer | capacitance | C _{rss} | V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz | ⁷ — | 7 | | pF |
| Output capacitance | | Coss | | _ | 70 | | |
| Switching time | Rise time | t _r | 10 V I _D = 2.5 A V _{OUT} | - (| 10 | | |
| | Turn-on time | t _{on} | V_{GS} V | | 20 |) — | |
| | Fall time | t _f | // // // V _{DD} ≈ 225 V | 7 | > 10 | | ns |
| | Turn-off time | t _{off} | V _{DD} = 223 V Duty ≦ 1%, t _W = 10 μs | | 50 | _ | |
| Total gate charge | · · | Qg | |) — | 16 | | |
| Gate-source charge Q _{gs} | | Q _{gs} | $V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$ | _ | 10 | _ | nC |
| Gate-drain charge Q _{g0} | | Q _{gd} | | | 6 | | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|--------------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) |)) I _{DR} | | _ | _ | 5 | Α |
| Pulse drain reverse current (Note 1) | I _{DRP} | | _ | _ | 20 | Α |
| Forward voltage (diode) | V _{DSF} | I _{DR} = 5 A, V _{GS} = 0 V | _ | _ | -1.7 | V |
| Reverse recovery time | ţr, | $I_{DR} = 5 A$, $V_{GS} = 0 V$, | _ | 1400 | | ns |
| Reverse recovery charge | Qrr | dl _{DR} /dt = 100 A/μs | _ | 9 | _ | μС |

Marking

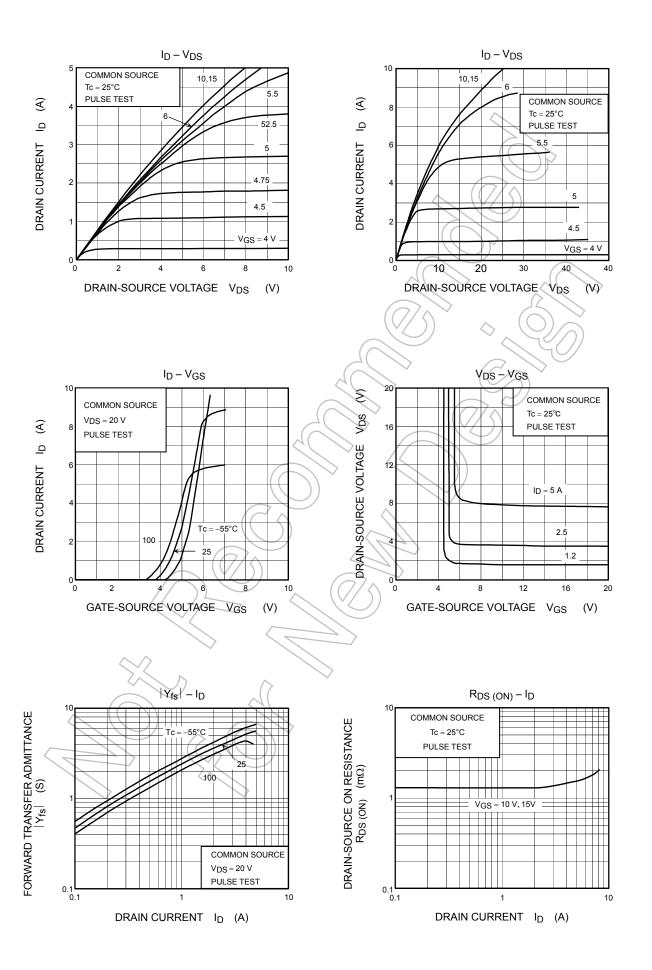


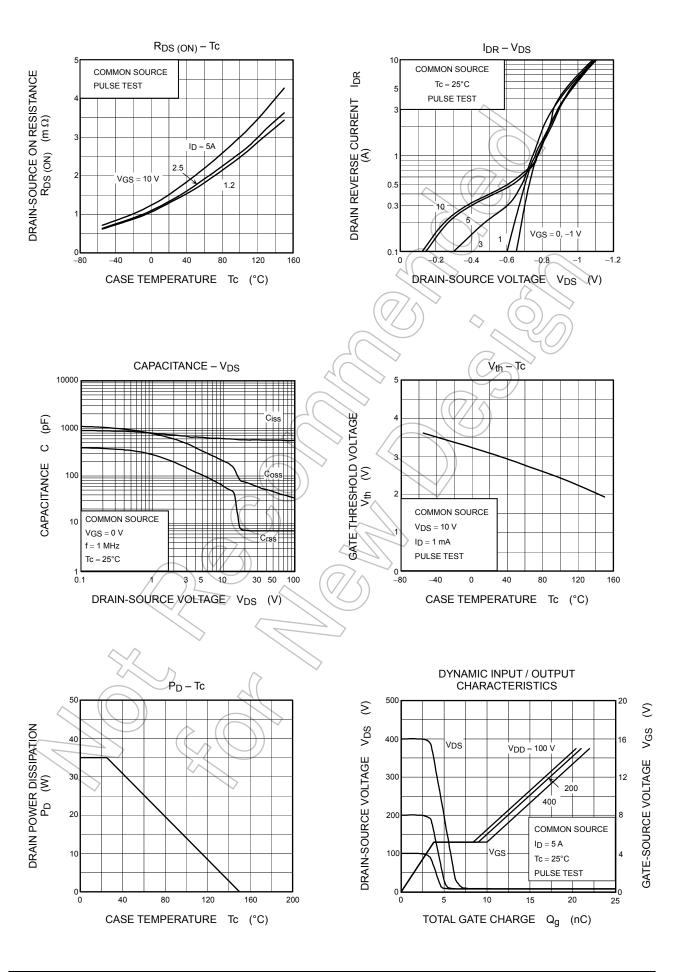
Note 4: A line under a Lot No. identifies the indication of product Labels.

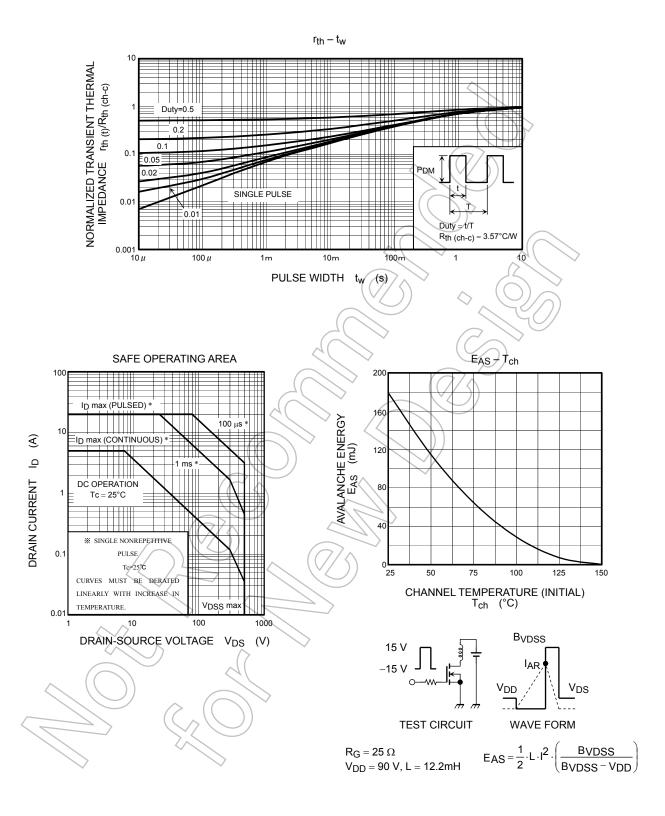
Not underlined: [[Pb]]/INCLUDES > MCV

 $\label{thm:compatible} \begin{tabular}{ll} Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]] \end{tabular}$

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.







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