TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2601

DC-DC Converter, Relay Drive and Motor Drive Applications

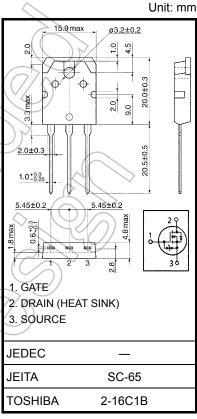
• Low drain–source ON-resistance : RDS (ON) = 0.56 Ω (typ.)

 $\begin{array}{ll} \bullet & \mbox{High forward transfer admittance} & : |Y_{fs}| = 7.0 \ \mbox{S (typ.)} \\ \bullet & \mbox{Low leakage current} & : I_{DSS} = 100 \ \mu\mbox{A (max) (V}_{DS} = 500 \ \mbox{V)} \\ \end{array}$

• Enhancement mode : $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	(Unit)
Drain-source voltage		V_{DSS}	500	V
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	500	V
Gate-source voltage		V_{GSS}	±30	> v
Drain current	DC (Note 1)	ΙD	(10)	Α
	Pulse (Note 1)	I _{DP}	40	A
Drain power dissipation	n (Tc = 25°C)	PD	125	/_w
Single pulse avalanche	e energy (Note 2)	EAS	270	mJ
Avalanche current		IAR	10	A
Repetitive avalanche e	nergy (Note 3)	(EAR))	12.5	μŊ
Channel temperature		Tch	150	°C
Storage temperature ra	ange	T _{stg}	-55 to 150	°C



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	1.0	°C / W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.59 mH, R_G = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

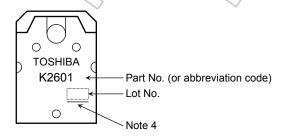
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I_{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bro	eakdown voltage	V _(BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	٧
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	7	_	100	μΑ
Drain-source br	eakdown voltage	V _{(BR) DSS}	I _D = 10 mA, V _{GS} = 0 V	500	1	_	V
Gate threshold v	/oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0) /_	4.0	V
Drain-source O	N-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 5 A,) 	0.56	1.0	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	3.5	7.0	_	S
Input capacitano	ce	C _{iss}			1200	_	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	200	_	pF
Output capacita	nce	Coss		_	550	_	
Switching time	Rise time	t _r	$V_{GS} \stackrel{10 \text{ V}}{\text{O} \text{ V}} \prod_{\text{I}} \stackrel{\text{I}}{\text{O}} = 5.\text{A}$	-	30	γ Ι _~	
	Turn-on time	t _{on}	R _L = 60 Ω		50	_	ns
	Fall time	t _f	$V_{DD} = 300 \text{ V}$		45	ı	113
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm w} = 10 \mu \rm s$) –	180	-	
Total gate char plus gate-drain)	rge (gate-source	Qg		_	30	_	
Gate-source ch	arge	Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$	_	15	_	nC
Gate-drain ("mil	ller") Charge	Q _{gd}		_	15	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	40	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 10 A, V _{GS} = 0 V	_	500	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 A / μs	_	4.3	_	μC

Marking

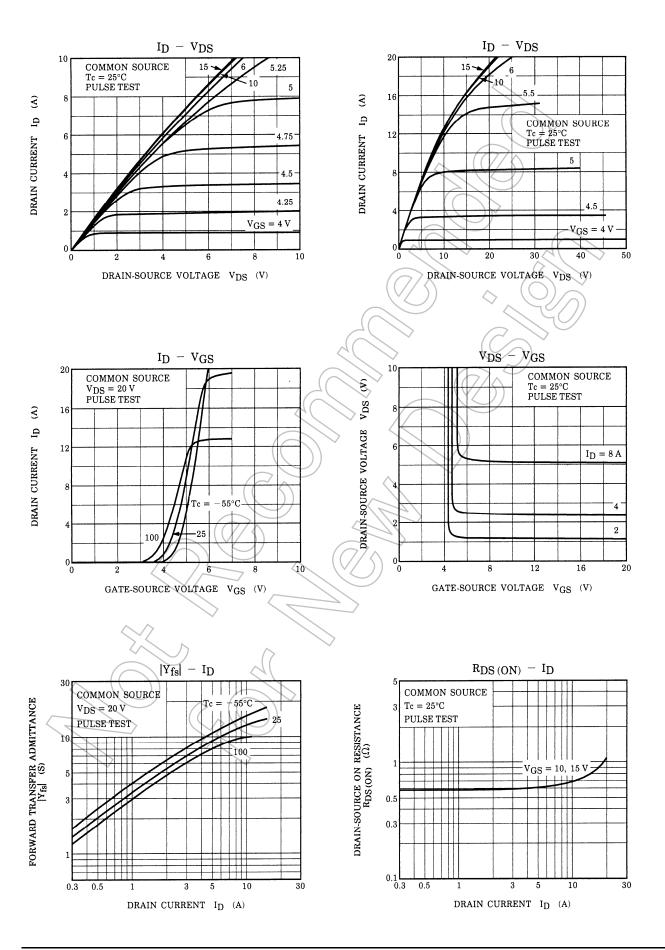


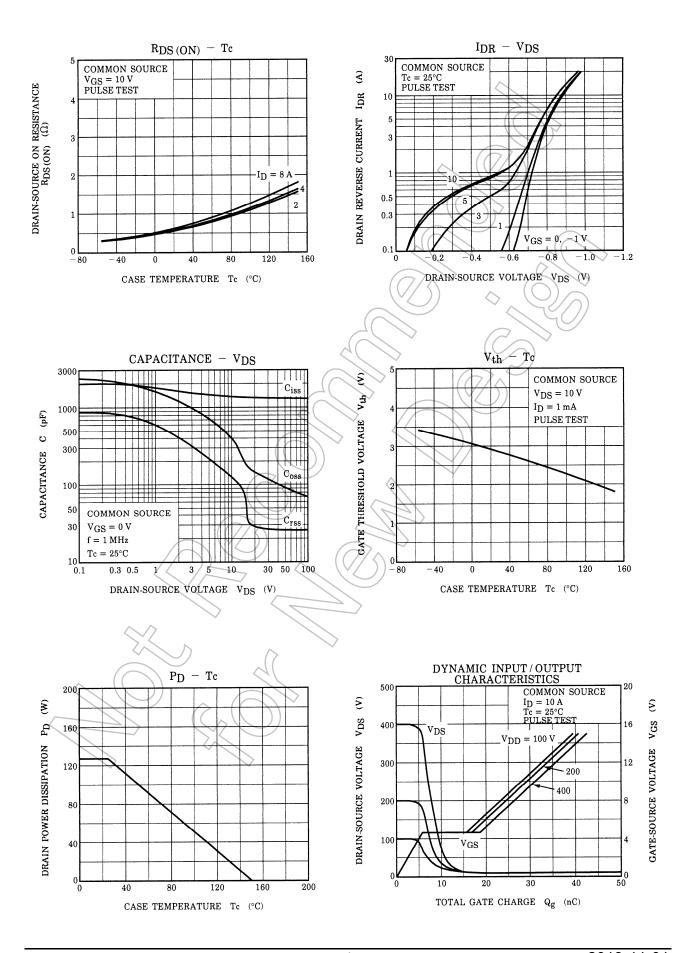
Note 4: A line under a Lot No. identifies the indication of product Labels.

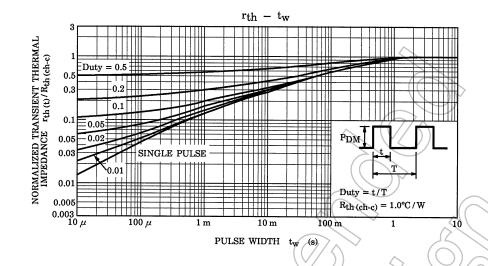
Not underlined: [[Pb]]/INCLUDES > MCV

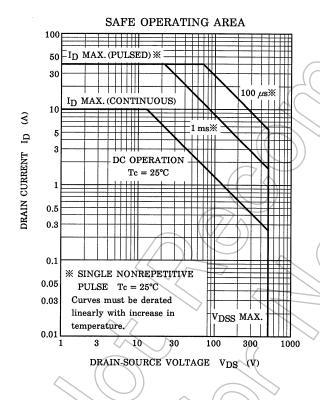
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

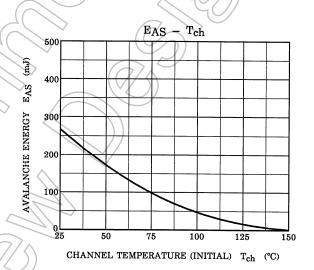
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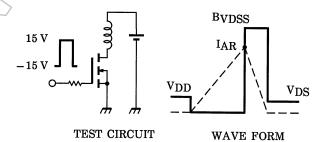












$$\begin{split} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 4.59~mH \end{split}$$

$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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