



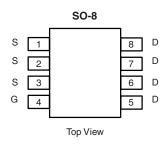
N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
30	0.024 at V _{GS} = 10 V	8.1		
	0.033 at V _{GS} = 4.5 V	6.9		

FEATURES

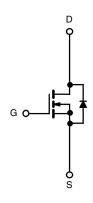
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si9410BDY-T1-E3 (Lead (Pb)-free)

Si9410BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$I_A = 25$ °C, unles	ss otherwise n	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	8.1	6.2	٨
	T _A = 70 °C		6.5	5.0	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	30		Α
Continuous Source Current (Diode Conduction) ^a		I _S	2.1	1.2	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D 2.5 1.6	2.5	1.5	10/
	T _A = 70 °C		0.9	W	
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manifestore bounding to Aughing 18	t ≤ 10 s	- R _{thJA}	40	50	°C/W
Maximum Junction-to-Ambient ^a	Steady State		70	85	
Maximum Junction-to-Foot	Steady State	R_{thJF}	20	24	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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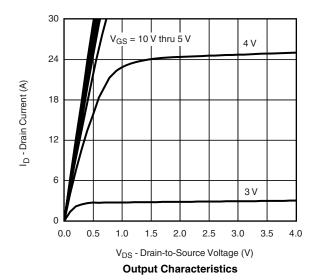
SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0		3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = 30 V, V _{GS} = 0 V	1 5				
	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
	D	V _{GS} = 10 V, I _D = 8.1 A	0.019		0.024	0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 6.9 \text{ A}$		0.026	0.033	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 8.1 A		20		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 2.1 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			15	23		
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 8.1 \text{ A}$		3.2		nC	
Gate-Drain Charge	Q_{gd}			2.5			
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		15	25		
Turn-Off Delay Time	t _{d(off)}	$t_{d(off)}$ $I_D \cong 1 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 6 \Omega$		30	45	ns	
Fall Time	t _f			11	20		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.1 A, dI/dt = 100 A/μs		25	50		

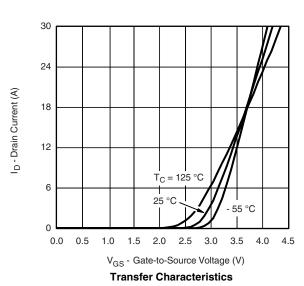
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



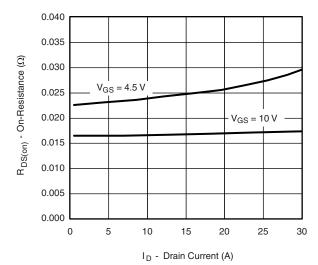




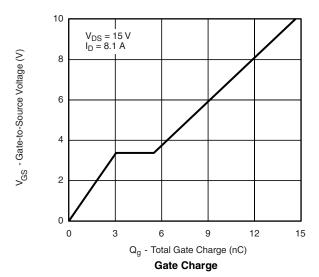


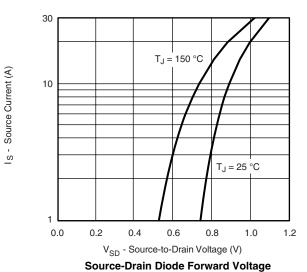


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



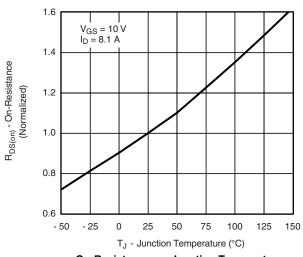
On-Resistance vs. Drain Current



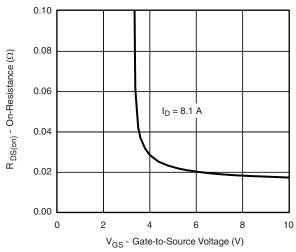


1200 1000 Ciss C - Capacitance (pF) 800 600 400 Coss 200 C_{rss} 0 0 10 15 20 25 30

 V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



On-Resistance vs. Junction Temperature

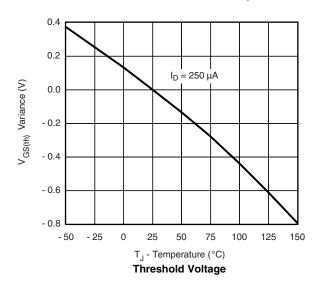


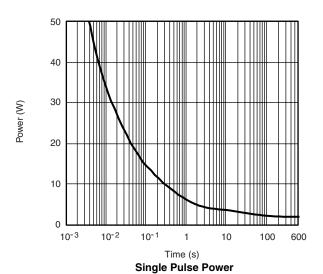
On-Resistance vs. Gate-to-Source Voltage

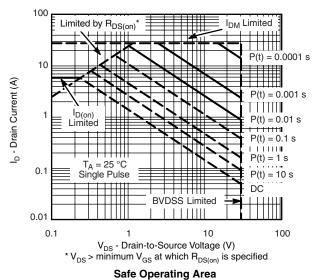
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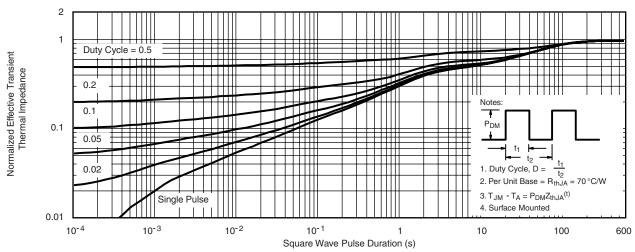
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





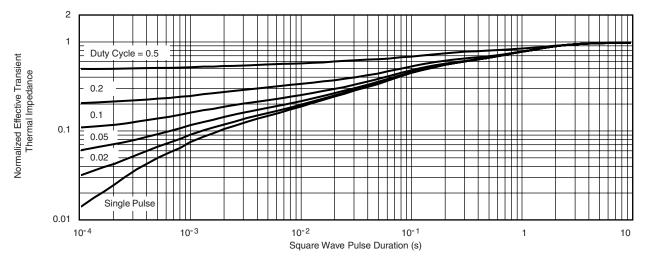




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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