

REPLACEMENT
of LM324

MIK324

LOW POWER QUAD OPERATIONAL AMPLIFIERS

CONTENTS	Page	CONTENTS	Page
GENERAL DESCRIPTION	1	ELECTRICAL CHARACTERISTICS	3
FEATURES	1	PHYSICAL DIMENSIONS AND MARKING DIAGRAMS	4
SCHEMATIC DIAGRAM	2	SOP-14	4
PIN CONNECTIONS	2	DIP-14	4
ABSOLUTE MAXIMUM RATINGS	2	ORDERING INFORMATION	4

GENERAL DESCRIPTION

The MIK324 consists of four independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks and all the conventional op amp circuits which now can be more easily implemented in single power supply systems.

For example, the MIK324 can be directly operated off of the standard +5V power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional $\pm 15V$ power supplies.

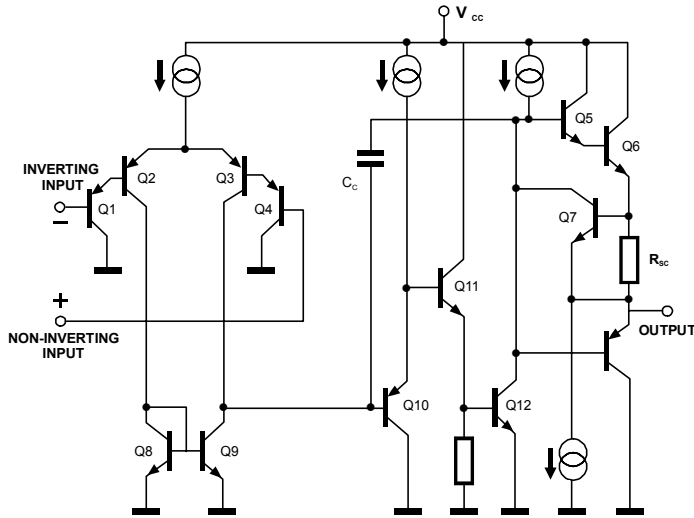
FEATURES


- Wide range of supply voltages 3V to 30V
- Low supply current drain independent of supply voltage 1.5mA TYP.
- Low input biasing current
- Low input offset voltage and offset current
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- DC voltage gain 100V/mV TYP.
- Internally frequency compensation



MIKRON JSC • <http://www.mikron.ru> • March 2003

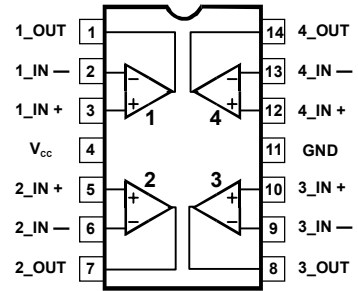
SCHEMATIC DIAGRAM (1/4 MIK324)



SOP-14  MIK324M

DIP-14  MIK324N

PIN CONNECTIONS (top view)



ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	VALUE	UNIT
POWER SUPPLY VOLTAGE			
V_{CC}	Single supply	32	V
V_{CC}, V_{EE}	Split supply	± 16	V
V_{IDR}	Input differential voltage range	± 32	V
I_{OS}	Output short-circuit to GND	Continuous	
T_J	Junction temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature	-65 to +150	$^{\circ}C$
T_A	Operating ambient temperature range	0 to +70	$^{\circ}C$



ELECTRICAL CHARACTERISTICS

at specified free-air temperature, $V_{CC} = 5\text{ V}$ (unless otherwise noted)

SYMBOL	PARAMETER	TEST CONDITIONS*	MIN	TYP	MAX	UNIT	
V_{IO}	Input offset voltage	$V_{CC} = 5\text{ V to MAX}$, $V_{IC} = V_{ICR\text{ min}}$, $V_O = 1.4\text{ V}$	25 °C		3	7	mV
			Full range			9	
αV_{IO}	Average temperature coefficient of input offset voltage		Full range		7	$\mu\text{V}/^\circ\text{C}$	
I_{IO}	Input offset current	$V_O = 1.4\text{ V}$	25 °C		2	50	nA
			Full range			150	
αI_{IO}	Average temperature coefficient of input offset current		Full range		10	$\text{pA}/^\circ\text{C}$	
I_{IB}	Input bias current	$V_O = 1.4\text{ V}$	25 °C		-20	-250	nA
			Full range			-500	
V_{ICR}	Common-mode input voltage range	$V_{CC} = 5\text{ V to MAX}$	25 °C	0 to $V_{CC} - 1.5$			V
			Full range	0 to $V_{CC} - 2.0$			
V_{OH}	High-level output voltage	$R_L = 2\text{ k}\Omega$	25 °C	$V_{CC} - 1.5$			V
		$V_{CC} = \text{MAX}$, $R_L = 2\text{ k}\Omega$	Full range	26			
		$V_{CC} = \text{MAX}$, $R_L = 10\text{ k}\Omega$	Full range	27	28		
V_{OL}	Low-level output voltage	$R_L = 10\text{ k}\Omega$	Full range		5	20	mV
A_{VD}	Large-signal differential voltage amplification	$V_{CC} = 15\text{ V}$, $V_O = 1\text{ V to } 11\text{ V}$, $R_L \geq 2\text{ k}\Omega$	25 °C	25	100		V/mV
			Full range	15			
CMRR	Common-mode rejection ratio	$V_{CC} = 5\text{ V to MAX}$, $V_{IC} = V_{ICR\text{ min}}$	25 °C	65	80		dB
K_{SVR}	Supply voltage rejection ratio ($\Delta V_{CC}/\Delta V_{IO}$)	$V_{CC} = 5\text{ V to MAX}$	25 °C	65	100		dB
V_{O1}/V_{O2}	Crosstalk attenuation	$f = 1\text{ kHz to } 20\text{ kHz}$	25 °C		120		dB
I_O	Output current	$V_{CC} = 15\text{ V}$, $V_{ID} = 1\text{ V}$, $V_O = 0$	25 °C	-20	-30		mA
			Full range	-10			
			25 °C	10	20		
			Full range	5			
I_{OS}	Short-circuit output current	V_{CC} at 5 V, GND at -5 V, $V_O = 0$	25 °C		± 40	± 60	mA
			Full range				
I_{CC}	Supply current (two amplifiers)	$V_O = 2.5\text{ V}$, No load	Full range		1.5	2.4	mA
		$V_{CC} = \text{MAX}$, $V_O = 0.5V_{CC}$, No load	Full range		1.1	3	

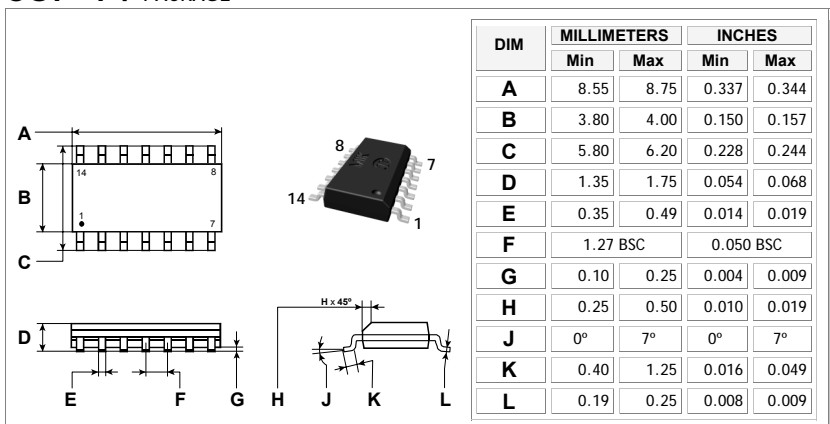
* All characteristics are measured under open-loop conditions with zero common-mode input voltage unless otherwise specified. "MAX" V_{CC} for testing purposes is 30 V. Full range is 0 °C to 70 °C.



MIKRON JSC • http://www.mikron.ru • March 2003

PHYSICAL DIMENSIONS AND MARKING DIAGRAMS

SOP-14 PACKAGE



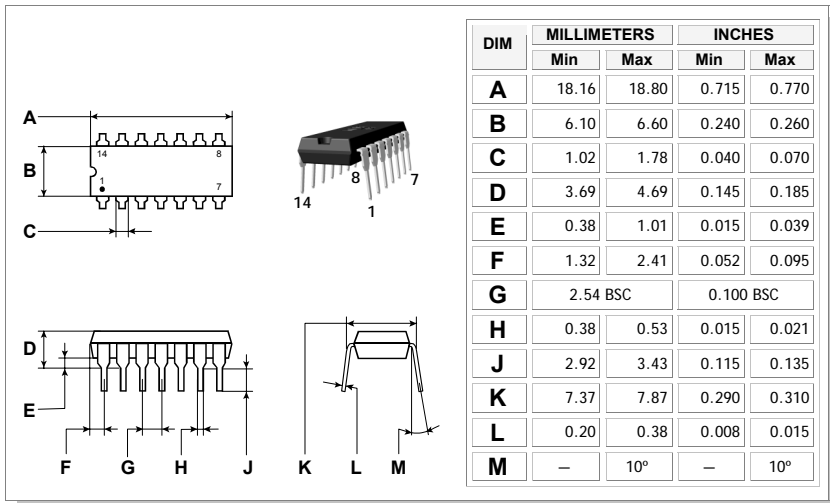
SOP-14

MARKING DIAGRAM



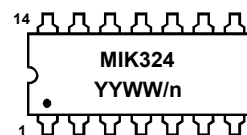
YY	Year
WW	Work Week
n	Assembly Location

DIP-14 PACKAGE



DIP-14

MARKING DIAGRAM



YY	Year
WW	Work Week
n	Assembly Location

ORDERING INFORMATION

ORDERING NUMBER	PACKAGE	OPERATING TEMPERATURE RANGE	SHIPPING
MIK324M	SOP-14	0 to +70°C	Tube/Rail and Tape/Reel
MIK324N	DIP-14		Tube/Rail and Tape/Reel

NOTE: THE FORM OF PACKING IS STIPULATED IN THE CONTRACT

The information presented in this Data sheet is believed to be accurate and reliable. Application circuits shown are typical examples illustrating the operation of the device. In the interest of product improvement, MIKRON reserves the right to change

specifications and data without notice and can assume no responsibility for the use of any information, devices and application circuits described herein. Reference to products of other manufacturers are solely for convenience and do not imply total equivalency of design, performance, or otherwise.

MIKRON JSC Head Office

Address: 1st Zapadny Proezd 12, Building 1, Zelenograd, Moscow, Russia, 124460
 Telephone: +7 (095) 535-23-43; 536-85-44
 Fax: +7 (095) 530-92-01
 Email: export@mikron.ru

MIKRON ShenZhen Office

Tel/Fax: +86-755-329-7574
 Voice: +86-755-329-7573
 Email: miksz@963.net