SM05T1G Series, SZSM05T1G

Transient Voltage Suppressor Diode Array

SOT-23 Dual Common Anode Diodes for ESD Protection

These dual monolithic silicon TVS diodes are designed for applications requiring transient overvoltage protection capability. They are intended for use in voltage and ESD sensitive equipment such as computers, printers, business machines, communication systems, medical equipment and other applications. Their dual junction common anode design protects two separate lines using only one package. These devices are ideal for situations where board space is at a premium.

Specification Features:

- SOT-23 Package Allows Either Two Separate Unidirectional Configurations or a Single Bidirectional Configuration
- Working Peak Reverse Voltage Range 5.0 V to 24 V
- Peak Power 300 Watt (8 X 20 μs)
- Low Leakage
- Flammability Rating UL 94 V-0
- SZ Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These are Pb-Free Devices*

Mechanical Characteristics:

CASE: Void-Free, Transfer-Molded, Thermosetting Plastic Case **FINISH:** Corrosion Resistant Finish, Easily Solderable

MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES: 260°C for 10 Seconds

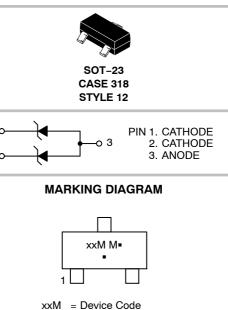
Package Designed for Optimal Automated Board Assembly Small Package Size for High Density Applications Available in 8 mm Tape and Reel

Use the Device Number to Order the 7 Inch/3,000 Unit Reel Replace the "T1" with "T3" in the Device Number to Order the 13 Inch/10,000 Unit Reel



ON Semiconductor®

http://onsemi.com



xx = 05, 12, 15, 24, 36

= Date Code*

Μ

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
SM05T1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
SZSM05T1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
SM12T1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
SM15T1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
SM24T1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
SM36T1G	SOT-23 (Pb-Free)	3,000/Tape & Reel

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

SM05T1G Series, SZSM05T1G

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation @ 20 μ s (Note 1) @ T _L \leq 25°C	P _{pk}	300	W
IEC 61000–4–2 (ESD) Air Contact		±15 ±8.0	kV
IEC 61000-4-4 (EFT)		40	А
IEC 61000-4-5 (Lightening)		12	А
Total Power Dissipation on FR-5 Board (Note 2) @ T _A = 25°C Derate above 25°C Thermal Resistance, Junction-to-Ambient	P _D R _{θJA}	225 1.8 556	mW mW/°C °C/W
Total Power Dissipation on Alumina Substrate (Note 3) @ T _A = 25°C Derate above 25°C Thermal Resistance, Junction-to-Ambient	P _D R _{θJA}	300 2.4 417	mW mW/°C °C/W
Junction and Storage Temperature Range	T _J , T _{stg}	– 55 to +150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Non-repetitive current pulse per Figure 3

2. FR–5 = 1.0 x 0.75 x 0.62 in.

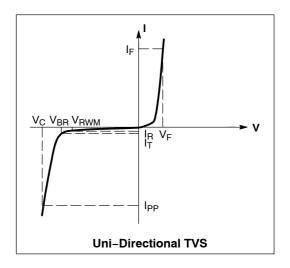
3. Alumina = 0.4 x 0.3 x 0.024 in., 99.5% alumina

NOTE: Other voltages may be available upon request

ELECTRICAL CHARACTERISTICS

UNIDIRECTIONAL (Circuit tied to Pins 1 and 3 or 2 and 3)

Symbol	Parameter				
I _{PP}	Maximum Reverse Peak Pulse Current				
V _C	Clamping Voltage @ IPP				
V _{RWM} Working Peak Reverse Voltage					
I _R	Maximum Reverse Leakage Current @ V _{RWM}				
V _{BR}	Breakdown Voltage @ I _T				
Ι _Τ	Test Current				
ΘV_{BR}	Maximum Temperature Coefficient of V_{BR}				
١ _F	Forward Current				
V _F	Forward Voltage @ I _F				
Z _{ZT}	Maximum Zener Impedance @ I _{ZT}				
I _{ZK}	Reverse Current				
Z _{ZK}	Maximum Zener Impedance @ I _{ZK}				



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

				V _{BR} , Breakd	own Voltage		V _C @ I _{PP} =	Max I _{PP}	Typical Capacitance
		V _{RWM}	I _R @ V _{RWM}	(Volts)		Ιτ	1 Amp	(Note 4)	(pF)
Device*	Device Marking	(Volts)	(μA)	Min	Max	mA	(Volts)	(Amps)	Pin 1 to 3 @ 0 Volts
SM05T1G	05M	5	10	6.2	7.3	1.0	9.8	17	225
SM12T1G	12M	12	1.0	13.3	15.75	1.0	19	12	95
SM15T1G	15M	15	1.0	16.7	19.6	1.0	24	10	100
SM24T1G	24M	24	1.0	26.7	31.35	1.0	43	5.0	60
SM36T1G	36M	36	1.0	40.0	46.95	1.0	60	4.0	45

4. $8 \times 20 \ \mu s$ pulse waveform per Figure 3

*Include SZ-prefix devices where applicable.

SM05T1G Series, SZSM05T1G

TYPICAL CHARACTERISTICS

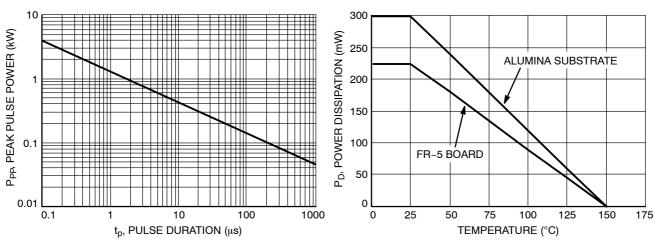


Figure 1. Non-Repetitive Peak Pulse Power versus Pulse Time

Figure 2. Steady State Power Derating Curve

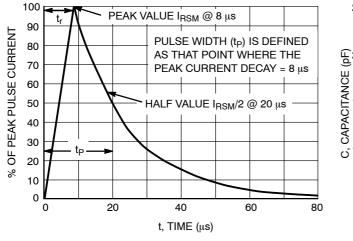


Figure 3. 8 \times 20 μs Pulse Waveform

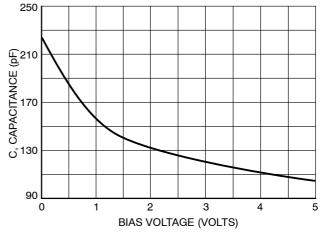
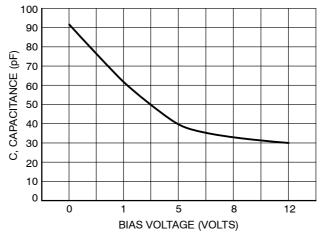


Figure 4. Typical Diode Capacitance (SM05)



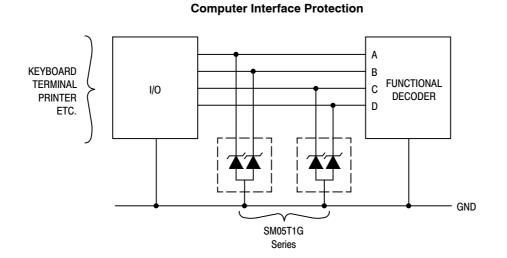


SM05T1G Series, SZSM05T1G

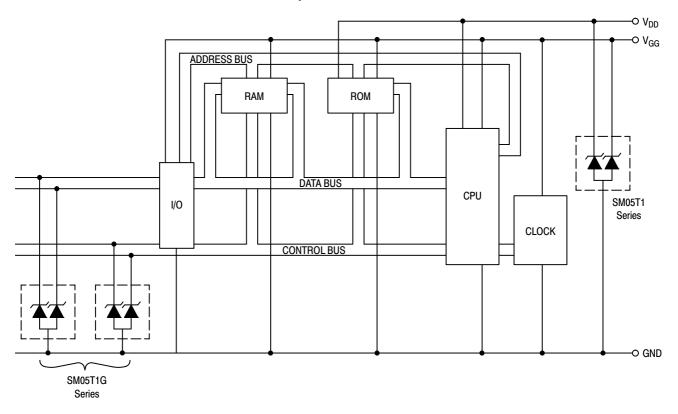
TYPICAL COMMON ANODE APPLICATIONS

A quad junction common anode design in a SOT-23 package protects four separate lines using only one package. This adds flexibility and creativity to PCB design especially

when board space is at a premium. Two simplified examples of TVS applications are illustrated below.

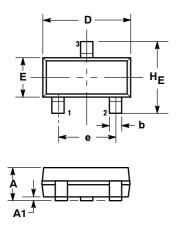


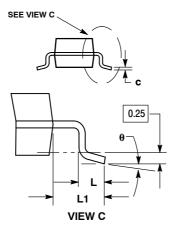
Microprocessor Protection



PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AP**





NOTES:

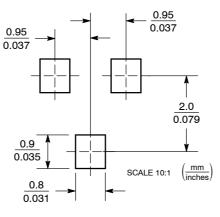
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH 2
- 3 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. 4

	м	ILLIMETE	RS	INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.89	1.00	1.11	0.035	0.040	0.044	
A1	0.01	0.06	0.10	0.001	0.002	0.004	
b	0.37	0.44	0.50	0.015	0.018	0.020	
с	0.09	0.13	0.18	0.003	0.005	0.007	
D	2.80	2.90	3.04	0.110	0.114	0.120	
E	1.20	1.30	1.40	0.047	0.051	0.055	
е	1.78	1.90	2.04	0.070	0.075	0.081	
L	0.10	0.20	0.30	0.004	0.008	0.012	
L1	0.35	0.54	0.69	0.014	0.021	0.029	
HE	2.10	2.40	2.64	0.083	0.094	0.104	
θ	0°		10°	0°		10°	

STYLE 12: PIN 1. CATHODE 2. CATHODE

3 ANODE

SOLDERING FOOTPRINT



ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILC does not convey any license under its patent rights or the rights of others. SCILC products are not designed, intended, or authorized for use a components in systems intended for surgical implant into the body, or other applications. intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative