

# FMI13N60ES

**FUJI POWER MOSFET** 

# Super FAP-E<sup>3S</sup> series

## **N-CHANNEL SILICON POWER MOSFET**

#### ■ Features

Maintains both low power loss and low noise Lower R<sub>DS</sub>(on) characteristic More controllable switching dv/dt by gate resistance Smaller V<sub>GS</sub> ringing waveform during switching Narrow band of the gate threshold voltage (4.2±0.5V) High avalanche durability

### Applications

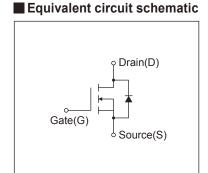
Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

# Maximum Ratings and Characteristics

# ● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

# T-Pack(L) 4 4 4 ① ② ③

■ Outline Drawings [mm]



Description	Symbol	Characteristics	Unit	Remarks
Drain Source Voltone	V <sub>DS</sub>	600	V	
Drain-Source Voltage	V <sub>DSX</sub>	600	V	V <sub>GS</sub> = -30V
Continuous Drain Current	ID	±13	Α	
Pulsed Drain Current	I <sub>DP</sub>	±52	Α	
Gate-Source Voltage	V <sub>GS</sub>	±30	V	
Repetitive and Non-Repetitive Maximum AvalancheCurrent	Iar	13	Α	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	471.5	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	22.5	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	4.7	kV/μs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Maximum Power Dissipation	Po	1.67	14/	Ta=25°C
		225	W	Tc=25°C
Operating and Storage Temperature range	Tch	150	°C	
	Tstg	-55 to + 150	°C	

## ● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions	Conditions		typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V		600	-	-	V
Gate Threshold Voltage	V <sub>GS</sub> (th)	In=250µA, Vns=Vgs	I <sub>D</sub> =250µA, V <sub>DS</sub> =V <sub>GS</sub>		4.2	4.7	V
Zero Gate Voltage Drain Current		V <sub>DS</sub> =600V, V <sub>GS</sub> =0V	T <sub>ch</sub> =25°C	-	-	25	μA
	IDSS	V <sub>DS</sub> =480V, V <sub>GS</sub> =0V	T <sub>ch</sub> =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V		10	100	nA
Drain-Source On-State Resistance	Ros (on)	I <sub>D</sub> =6.5A, V <sub>GS</sub> =10V	I <sub>D</sub> =6.5A, V <sub>GS</sub> =10V		0.50	0.58	Ω
Forward Transconductance	<b>g</b> fs	I <sub>D</sub> =6.5A, V <sub>DS</sub> =25V		5	10	-	S
nput Capacitance	Ciss	V <sub>DS</sub> =25V		-	1700	2550	pF
Output Capacitance	Coss	V <sub>GS</sub> =0V	V <sub>GS</sub> =0V		190	285	
Reverse Transfer Capacitance	Crss	f=1MHz		-	10	15	
Turn-On Time	td(on)	V <sub>cc</sub> =300V V <sub>ss</sub> =10V		-	38	57	no
	tr			-	24	36	
td(		I <sub>D</sub> =6.5A		-	86	129	ns
Turn-Off Time	tf	R <sub>G</sub> =18Ω		-	16	24	l
Total Gate Charge	Q <sub>G</sub>	1/ 0001/	V <sub>cc</sub> =300V I <sub>D</sub> =13A		48	72	nC
Gate-Source Charge	QGS				16	24	
Gate-Drain Charge	Q <sub>GD</sub>	U <sub>GS</sub> =10V			16	24	
Gate-Drain Crossover Charge	Qsw	VGS - 10 V		-	7	10.5	
Avalanche Capability	lav	L=2.36mH, Tch=25°C	L=2.36mH, Tch=25°C		-	-	Α
Diode Forward On-Voltage	V <sub>SD</sub>	I <sub>F</sub> =13A, V <sub>GS</sub> =0V, T <sub>ch</sub> =25°	I <sub>F</sub> =13A, V <sub>GS</sub> =0V, T <sub>ch</sub> =25°C		0.90	1.08	V
Reverse Recovery Time	trr	I <sub>F</sub> =13A, V <sub>GS</sub> =0V	I <sub>F</sub> =13A, V <sub>GS</sub> =0V		0.7	-	μS
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	8	-	μC

#### Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			0.560	°C/W
	Rth (ch-a)	Channel to ambient			75.0	°C/W

Note \*1 : Tch≤150°C

Note \*2: Stating Tch=25°C, Ias=6A, L=24.0mH, Vcc=60V, R<sub>G</sub>=50Ω

Eas limited by maximum channel temperature and avalanche current.

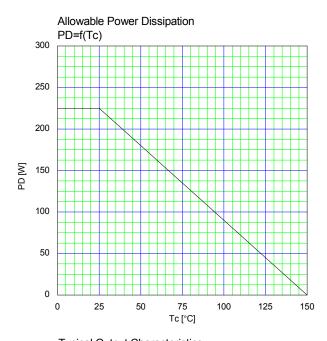
See to 'Avalanche Energy' graph.

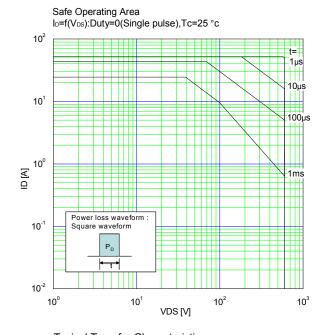
Note  $^{\star}3$  : Repetitive rating : Pulse width limited by maximum channel temperature

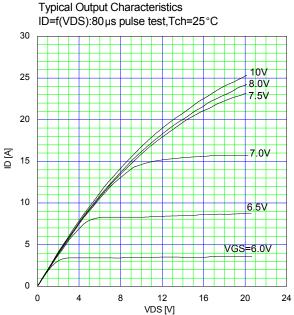
See to the 'Transient Themal impeadance' graph.

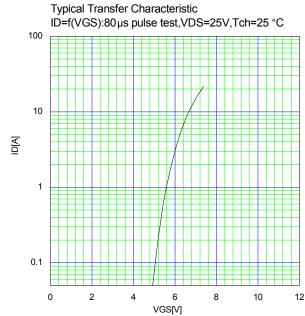
Note \*4 : Ir≤-Ip, -di/dt=100A/µs, Vcc≤BVbss, Tch≤150°C.

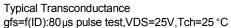
Note \*5 : Ir≤-Ip, dv/dt=4.7kV/µs, Vcc≤BVbss, Tch≤150°C.

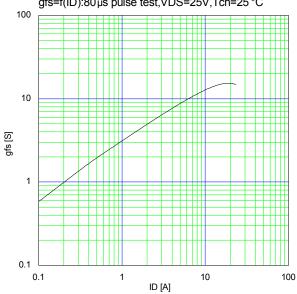


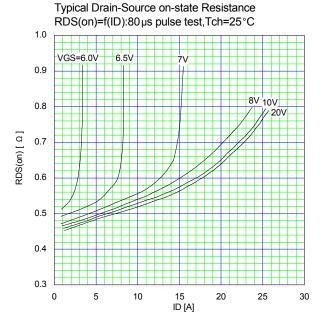




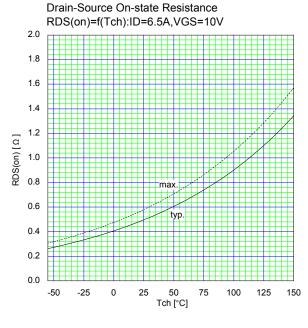


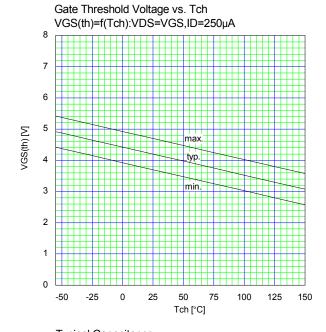


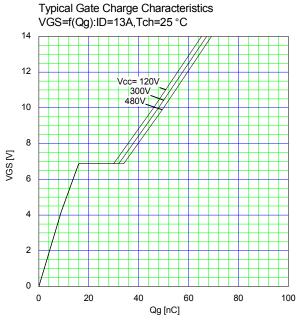


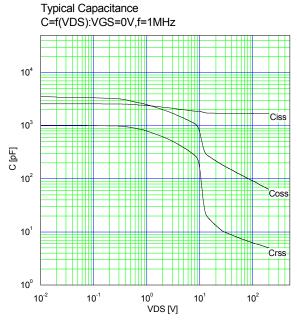


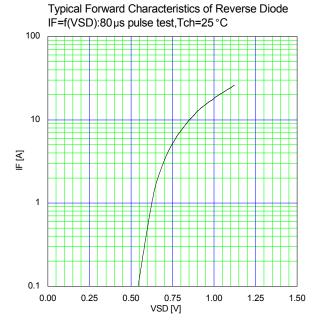
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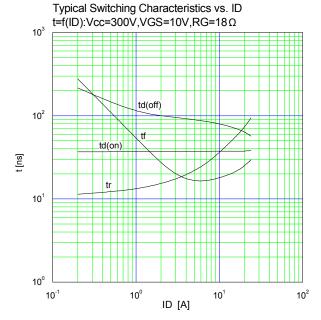


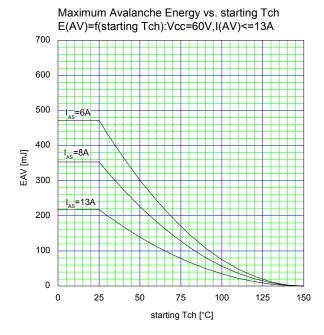


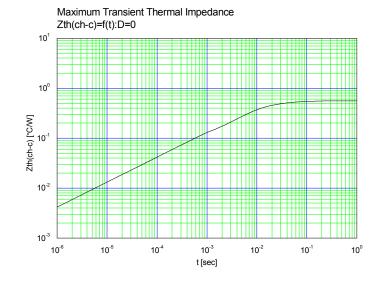












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