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### **■**General Descriptions

SI-8000TM series has 300kHz oscillation frequency, so it contributes miniaturization of a chalk coil.

Therefore, it realizes a compact and high efficient power supply together with TO 252-5 package (compatible:SC-63).

SI-8000TM series has the function required for switching regulators and protection circuits such as over-current, and overheating.

With only four discrete components, it realizes a high efficient switching regulator without adjustment.

1.5A output current by compact power surface mount package.

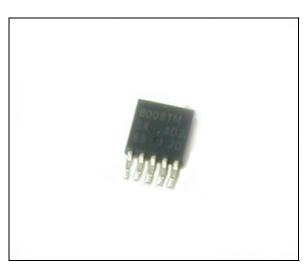
### **■**Applications

- •DVD Recorder、FPD TV
- •OA equipment, such as a printer
- On-board local power supply

### **■**Features

- •1.5A output current by compact power surface mount package.
- High Efficiency 81% (at VIN=15V, Io=0.5A, Vo=5V)
- Requires only four discrete components. (C3: required for soft start.)
- Built-in Oscillation circuit (oscillation frequency: 300kHz (TYP))
- Constant current type overcurrent protection and overheating protection
- Soft start function. (ON/OFF function available. Output OFF at Low level.)
- Low consumption current at Output OFF.

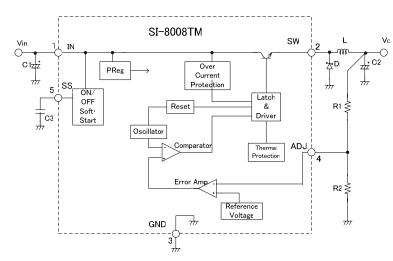
### ■Package---TO252-5



### **■**Key Specifications

	SI-8008T M (ADJ Type)	SI-8033T M	SI-8050T M	SI-8120T M			
Input Voltage	43V						
Output Current	0 to 1.5A						
Output Voltage	0.8V (REF Voltage)	3.3V	5.0V	12.0V			
Efficiency (TYP)	81% (5V Set-up)	76%	81%	87%			

### **Typical Connection**



 $C1:220\,\mu\,F$ 

 $C2:470\,\mu \, F$ 

 $C3:1 \mu F$ 

(At the time of soft start functional use)

L1 : 47  $\mu$  H

Di: SFPB66(Sanken)



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### 1. Scope

The present specifications shall apply to a DC-DC buck converter SI-8008TM.

### 2. Outline

Classification	Semiconductor IC (monolithic IC)
Structure	Plastic package (transfer mold)
Applications	<ul> <li>DC voltage regulator</li> <li>Power supplies for telecommunication equipment, office equipment etc, ON-board local regulator</li> <li>Output voltage regulator at secondary stage of switch mode power supply</li> </ul>

## 3. Absolute maximum ratings

### 3-1 Absolute maximum ratings

Characteristic	Symbol	Ratings	Units	Remarks
DC input voltage	VIN	43	V	
Power dissipation 1	Pd1	1 .06	W	Glass - epoxy board mounting in 900square mm, copper area 4.3% Tjmax=125°C
Power dissipation 2	Pd2	1.65	W	Glass - epoxy board mounting in 900square mm, copper area $50\%$ Tjmax= $125$ °C
Junction temperature	Тј	-40~150	$^{\circ}$	Thermal protection circuit is built-in in this product and when junction temperature rises to 130°C or higher, it may be caused to operate Recommended max. junction temperature at operation is 125°C.
Storage temperature	Tstg	-40~150	$^{\circ}$	
Thermal resistance (junction-case)	θ j-c	6	°C/W	
Thermal resistance (junction-ambient air)	θ ј-а	95	°C/W	Glass - epoxy board mounting in 900square mm, copper area 4.3%



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### 3-2 Recommended operating conditions

Characteristic	Symbol	Rati	ngs	Units	Remarks	
Characteristic	Symbol	MIN	MAX	Cilius	Ttemarks	
DC input voltage range	VIN	*1 Vo+3	40	V	Io=0∼1.5A	
DC output voltage range	Vo	0.8~24		V		
DC output current range	Io	0~1.5		A	*2 V <sub>IN</sub> ≧V <sub>0</sub> +3V	
Operating junction temperature range	Tjop	-40~125		$^{\circ}\!\mathbb{C}$		
Operating temperature range	Тор	-40~125		$_{\mathbb{C}}$	*2	

<sup>\*1</sup> The minimum value of DC input voltage range is 8V when output is less than 5V, Vo+3,when the output is more than 5V.In the case of VIN=Vo+2 $\sim$ Vo+3V , it is set to Io=1A MAX.

### 4. Electrical characteristics

4-1 Electrical characteristics (Ta=25 $^{\circ}$ C \, Vo=5V adjusted R1=4.2k  $\Omega$  ,R2=0.8k  $\Omega$  )

Characteristic		Symbol	MIN	Limits TYP	MAX	Units	Test conditions
Reference voltage		$V_{\mathrm{ADJ}}$	0.784	0.800	0.816	V	V <sub>IN</sub> =15V, I <sub>0</sub> =0.1A
Reference voltage temperature coefficient		∠V <sub>REF</sub> /∠T		±0.1		mV/°C	$V_{IN}$ =15V , $I_0$ =0.1A,Tc=0~100°C
Efficiency *3		η		81		%	V <sub>IN</sub> =15V,I <sub>O</sub> =0.5A
Operating frequency		fo		300		kHz	V <sub>IN</sub> =15V, I <sub>O</sub> =0.5A
Line regulati	Line regulation			60	80	mV	$V_{IN}=10\sim30V, I_0=0.5A$
Load regulat	Load regulation			10	40	mV	V <sub>IN</sub> =15V, I <sub>0</sub> =0.2~1.5A
Over current	Over current protection starting current		1.6			A	V <sub>IN</sub> =15V
ON/OFF	Low level voltage	$V_{\mathrm{SSL}}$			0.5	V	
terminal *4	Flow-out current at low level voltage	Issl		10	40	$\mu$ A	V <sub>SSL</sub> =0V
Quiescent current 1		$_{ m Iq}$		6		mA	V <sub>IN</sub> =15V, I <sub>O</sub> =0A
Quiescent current 2		Iq(off)		200	400	$\mu$ A	V <sub>IN</sub> =15V V <sub>SS</sub> =0V

<sup>\*2</sup> To be used within the allowable package power dissipation characteristics (refer to P6).



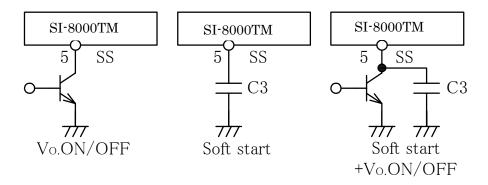
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Efficiency is calculated by the following equation.  $\eta \text{ (\%)=} \frac{V_0 \cdot I_0}{V_{IN} \cdot I_{IN}} \times 100$ \*3

$$\eta$$
 (%)=  $\frac{V_0 \cdot I_0}{V_{IN} \cdot I_{IN}} \times 100$ 

No.5 terminal is a SS terminal to enable soft start by connecting a capacitor. The output can \*4 be turned on and off by using a SS terminal. The output is stopped by decreasing the SS terminal voltage below Vssl and in order to perform ON/OFF operation of Vout, it is required to connect NPN transistor or the output of open collector type TTL between No.5 terminal and GND.

In case that both soft and Vout ON/OFF are used, a protection measure such as limitation of current is required, as the discharge current of C3 flows across a transistor for ON/OFF operation, if the capacitance of C3 large. As a pull-up type resistor is provided inside the IC, no external voltage can be applied. In case of no use of ON/OFF, please keep it open.

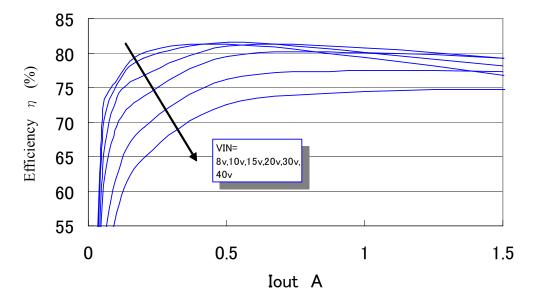


### 4-2 Typical characteristics (Ta=25°C)

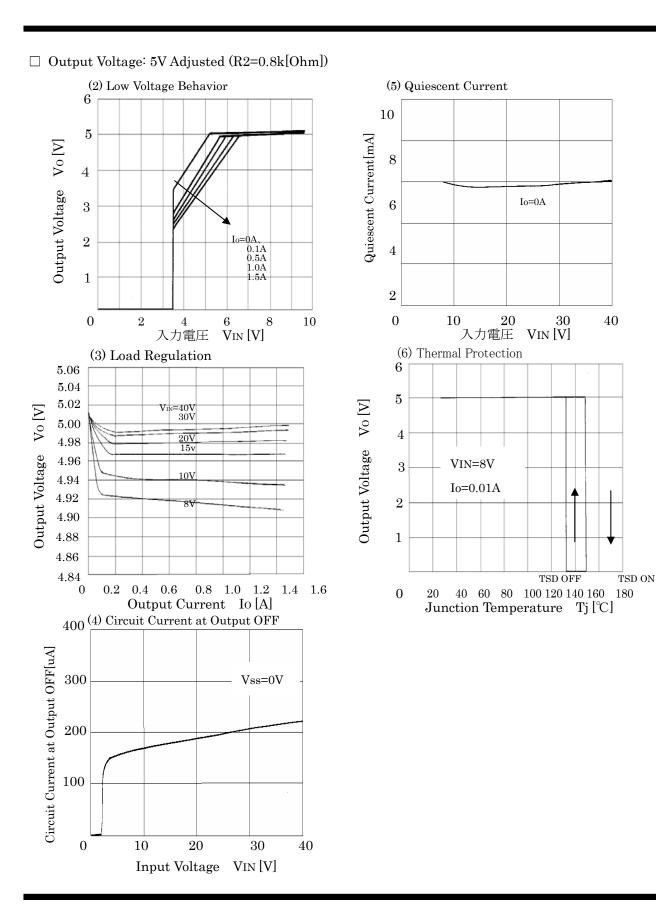
### (1) Efficiency

### SI-8008TM Efficiency Curve

Vo=5V Adjusted: R1=4.2kOhm, R2=0.8kOhm



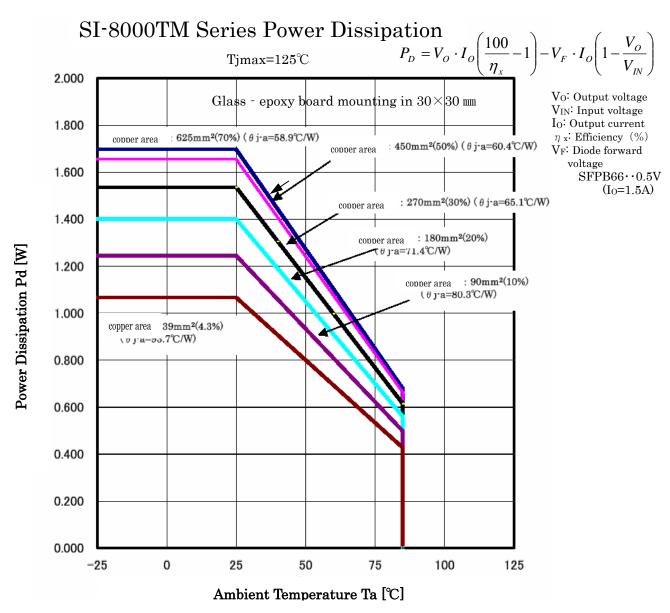
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4-2 Power Dissipation



Note1: As the efficiency varies subject to the input voltage and output current, it shall be obtained from the efficiency curve in page 4 and substituted in percent.

Note2: Thermal design for Di shall be made separately.

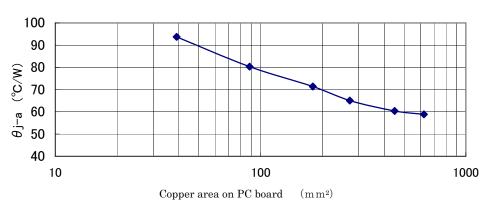


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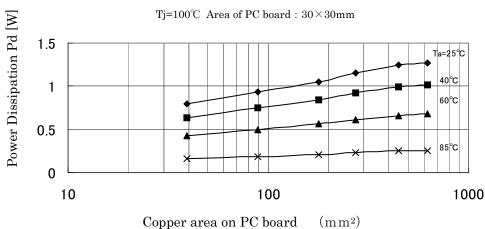
Reference data

SI-8000TM Copper area on PC board vs. thermal resistance.

Area of PC board :  $30 \times 30$ mm



### Copper area on PC board vs. Power dissipation

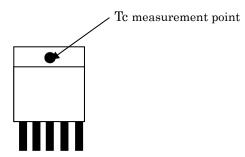


· Calculation of junction temperature

The junction temperature can be obtained from the following equation using the Tc that is measured by thermocouple.

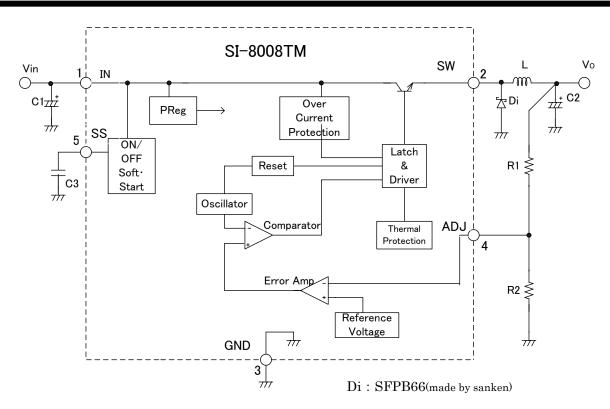
$$Tj = P_D \times \theta_{J-C} + T_C$$
  $(\theta \text{ j-c} = 6^{\circ}\text{C/W})$ 

5. Block diagram (Connection diagram)



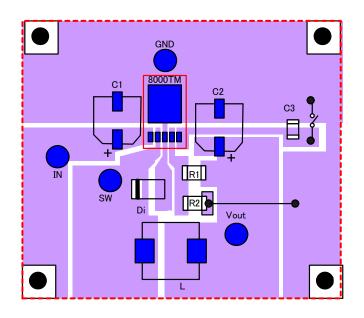


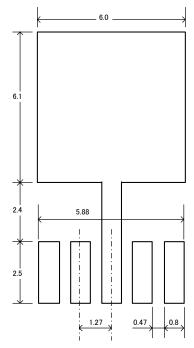
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Recommended pattern

The example of the solder pattern.





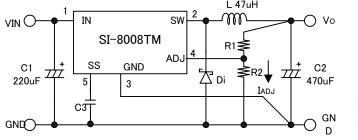
- \*The circuit board layout is recommended as follows:
- ①Other components are connected as close as possible to the SI-8008TM.
- ②Each ground of all components is connected at one point area.
- 6. Example application circuit



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### 6-1 Standard circuit diagram



 $C1:220\,\mu\,F$ 

 $C2:470\,\mu \, F$ 

C3 :  $1 \mu \, F$ 

(If soft start function is used)

 $L1:47 \mu H$ 

Di: SFPB66

(Made by Sanken)

### Diode D i

· The shottky-barrier diode must be used for Di. If other diodes like fast recovery diodes are used, IC may be destroyed because of the reverse voltage applied by the recovery voltage or ON voltage.

### Choke coil L1

- · If the winding resistance of the choke coil is too high, the efficiency may go down to the extent that it is out of the rating.
- · As the overcurrent protection start current is approx. 2.5A, attention must be paid to the heating of the choke coil by the magnetic saturation due to overload or short-circulated load.

### Capacitor C1, C2, C3

- · As large ripple currents flow across C1 and C2, capacitors with high frequency and low impedance for SMPS must be used. Especially when the impedance of C2 is high, the switching waveform may not be normal at low temperature. Please use neither OS capacitor nor tantalum capacitor which causes an abnormal oscillation for the C2.
- ·C3 is a capacitor for soft start. In case soft start function is not used, please keep No.5 terminal open. A pull-up resistor is provided inside the IC.

### Resister R1, R2

· R1, R2 is resistor to the Output Voltage. IADJ set to become 1mA. Moreover, R1, R2 is calculated by the following expression.  $R1 = \frac{(V_{OUT} - V_{ADJ})}{I_{ADJ}} = \frac{(V_{OUT} - 0.8)}{1 \times 10^{-3}} (\Omega), \quad R2 = \frac{V_{ADJ}}{I_{ADJ}} = \frac{0.8}{1 \times 10^{-3}} \stackrel{.}{=} 0.8k(\Omega)$ 

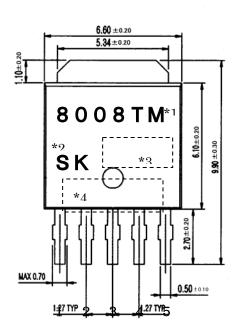
©In order to have optimum operating condition, each component must be laid out with the minimum distance.

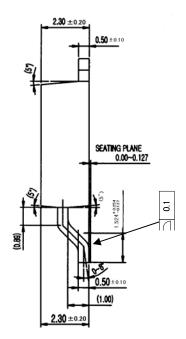


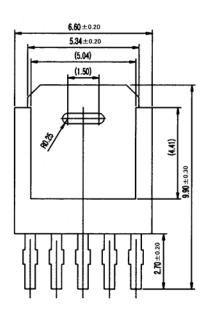
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### 7 Package information

7-1 Package type and dimensions







Pin assignment

1 : IN

2:SW

3 : GND

4:ADJ

5 : SS

\*1 Type No

\*2 Logo mark

\*3 Lot Number

1st letter: The last digit of year

 $2nd\ letter:Month$ 

1 to 9 for Jan. to Sept.

O for Oct. N for Nov. D for Dec.

3rd letter: week(Arabic Numerical)

\*4 Administer number (four digit)

### 7-2 Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

Products Weight: Approx.0.33g

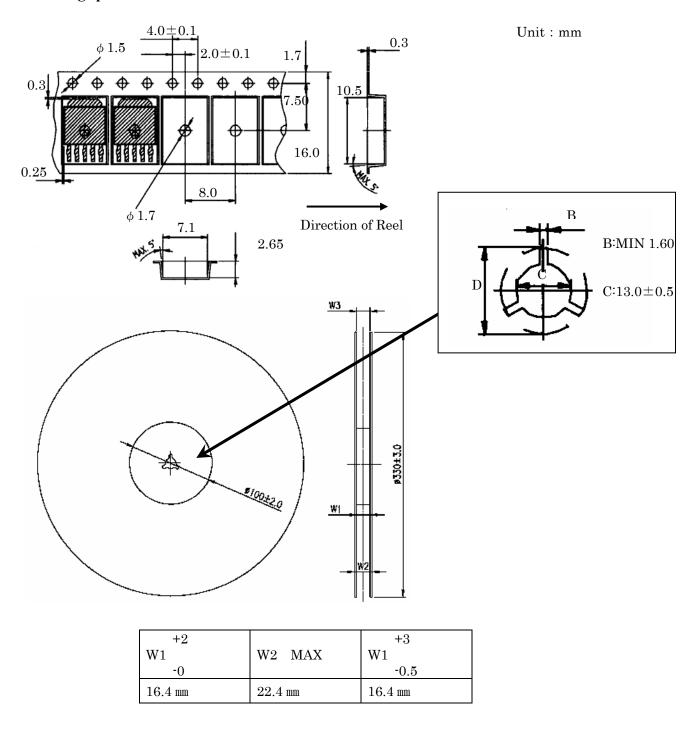
### 7-3 Marking

The type number and lot number shall be clearly stamped by laser on the body so that they cannot be erased easily.



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### 8. Packing specifications



Quantity: 3000pcs/reel



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### 9. Cautions and warnings

### 9-1 Parallel operation

The parallel operation to increase the current is not available.

### 9-2 Thermal protection

The SI-8000TM series has a thermal protection circuit. This circuit keeps the IC from the fever by the over load. But this circuit cannot guarantee the long-term reliability against the continuously over load status.



## SI-8000TM Series

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