

FMC16N50E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

Applications

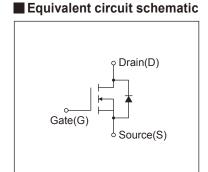
Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

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Fig. 1. 10-1-10-10-10-10-10-10-10-10-10-10-10-10	CONNECTION ① CATE ③ ② DRAIN ③ SOUNCE

■ Outline Drawings [mm]



Description	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	V _{DS}	500	V	
	V _{DSX}	500	V	V _{GS} = -30V
Continuous Drain Current	ID	±16	А	
Pulsed Drain Current	I _{DP}	±64	Α	
Gate-Source Voltage	V _{GS}	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	Iar	16	Α	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	485	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	22.5	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	7.8	kV/μs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Manifestore Barray Biogination	Po	1.67	10/	Ta=25°C
Maximum Power Dissipation		225	W	Tc=25°C
O	Tch	150	°C	
Operating and Storage Temperature range	Tstg	-55 to +150	°C	

Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	In=250µA, Vgs=0V	I _D =250μA, V _{GS} =0V		-	-	V	
Gate Threshold Voltage	V _{GS} (th)	In=250µA, Vns=Vgs	I _D =250μA, V _{DS} =V _{GS}		3.0	3.5	V	
Zava Cata Valtaga Drain Corrent	Ipss	V _{DS} =500V, V _{GS} =0V	T _{ch} =25°C	-	-	25		
Zero Gate Voltage Drain Current	IDSS	V _{DS} =400V, V _{GS} =0V	T _{ch} =125°C	-	-	250	μA	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V	V _{GS} =±30V, V _{DS} =0V		10	100	nA	
Drain-Source On-State Resistance	R _{DS} (on)	I _D =8A, V _{GS} =10V		-	0.33	0.38	Ω	
Forward Transconductance	g fs	I _D =8A, V _{DS} =25V		8.5	17	-	S	
Input Capacitance	Ciss	V _{DS} =25V		-	2150	3225		
Output Capacitance	Coss	V _{GS} =0V	V _{GS} =0V		210	315	pF	
Reverse Transfer Capacitance	Crss	f=1MHz -		16	24	1		
T O. Tim.	td(on)	V _{cc} =300V V _{ds} =10V I _D =8A		-	21	31.5	ns	
Turn-On Time	tr			-	9	13.5		
Turn-Off Time	td(off)			-	100	150		
	tf	R _{GS} =10Ω		-	16	24	1	
Total Gate Charge	QG	Vcc=250V	Vc=250V		60	90		
Gate-Source Charge	Qgs	ID=16A		-	17	25.5	nC	
Gate-Drain Charge	Q _{GD}	V _{GS} =10V	V _{GS} =10V		18	27		
Avalanche Capability	lav	L=1.52mH, Tch=25°C	L=1.52mH, Tch=25°C		-	-	Α	
Diode Forward On-Voltage	Vsp	I _F =16A, V _{GS} =0V, T _{ch} =25°	I _F =16A, V _{GS} =0V, T _{ch} =25°C		0.90	1.35	V	
Reverse Recovery Time	trr	I _F =16A, V _{GS} =0V		-	0.46	-	μs	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25	-di/dt=100A/µs, Tch=25°C		6.0	-	μC	

Thermal Characteristics

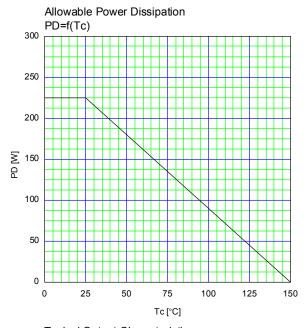
Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			0.560	°C/W
	Rth (ch-a)	Channel to Ambient			75.0	°C/W

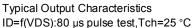
Note *1 : Tch≤150°C

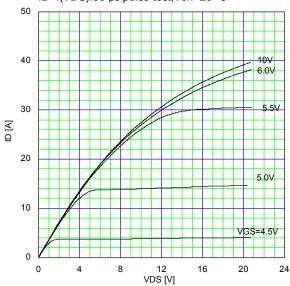
Note *2 : Stating Tch=25°C, Ias=7A, L=18.1mH, Vcc=50V, Rg=50 Ω Eas limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph. Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

See to the 'Transient Themal impeadance' graph.

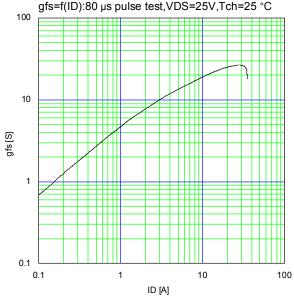
Note *4 : I_F≤-I_D, -di/dt=100A/μs, Vcc≤BV_{DSS}, Tch≤150°C. Note *5 : I_F≤-I_D, dv/dt=7.8kV/μs, Vcc≤BV_{DSS}, Tch≤150°C.



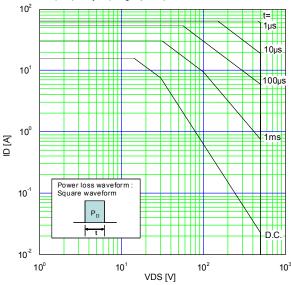




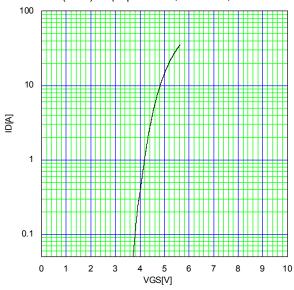
Typical Transconductance gfs=f(ID):80 μs pulse test,VDS=25V,Tch=25 $^{\circ} C$



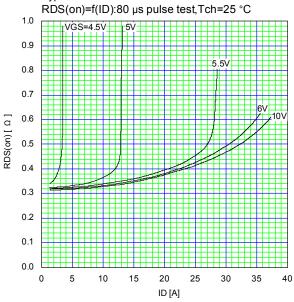
Safe Operating Area I_D =f(V_D s):Duty=0(Single pulse),Tc=25 °c

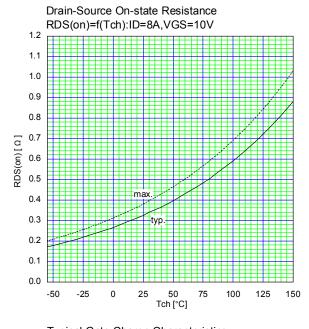


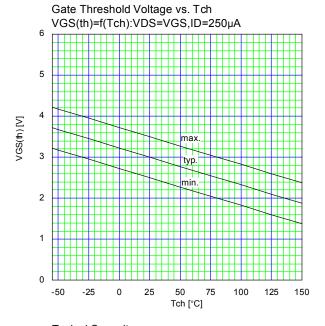
Typical Transfer Characteristic ID=f(VGS):80 µs pulse test,VDS=25V,Tch=25 °C

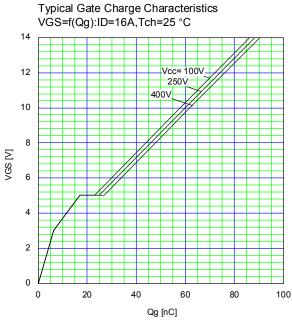


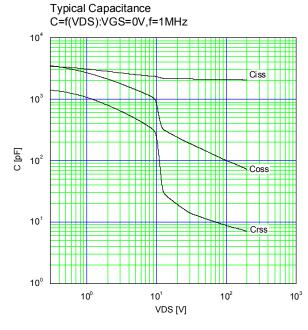
Typical Drain-Source on-state Resistance

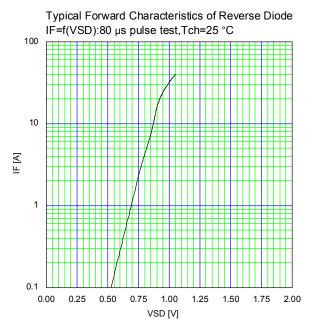


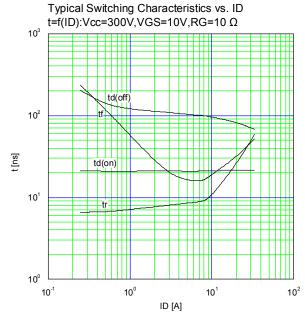


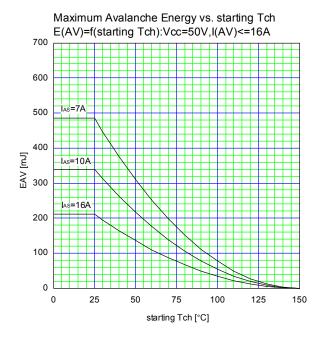


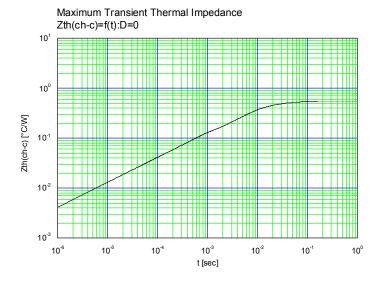












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