

P-Channel Enhancement Mode Field Effect Transistor

Features

 $V_{DS}(V) = -30V, I_D = -5.2A,$

 $R_{DS(ON)} = 51 \text{m} \Omega$ @Vgs = -10V.

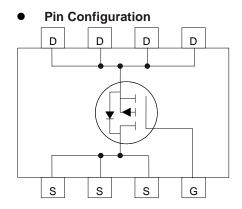
 $R_{DS(ON)} = 68 \text{m} \Omega$ @Vgs = -4.5V.

High density cell design for low RDS(ON).

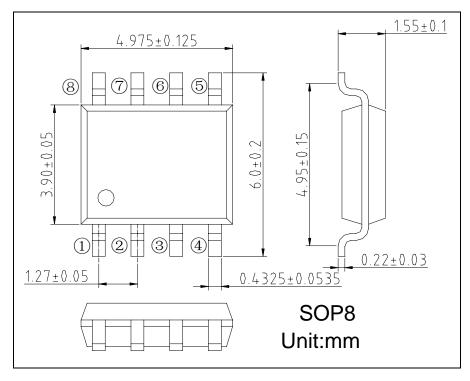
General Description

This P-Channel enhancement mode power FETs are produced with high cell density, DMOS trench technology, which is especially used to minimize on-state resistance. This device is particularly suited for low voltage application such as portable equipment, power management and other battery powered circuits, and low in-line power loss are needed in a

very small outline surface mount package.



Package Information



RCR1526SQ



■ Absolute Maximum Ratings @T_A = 25°C unless otherwise noted

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V_{DSS}	-30	V	
Gate-Source Voltage		V_{GSS}	±20	V	
Drain Current (Note 1)	Continuous T _A =25°C		-5.2	А	
	Pulsed (Note 2)	l _D	-50	А	
Total Power Dissipation (Note 1)		P _D	1.5	W	
Operating and Storage Junction Temperature Range		T _J , T _{STG}	-55 to +150	°C	

● Electrical Characteristics @TA = 25°C unless otherwise noted

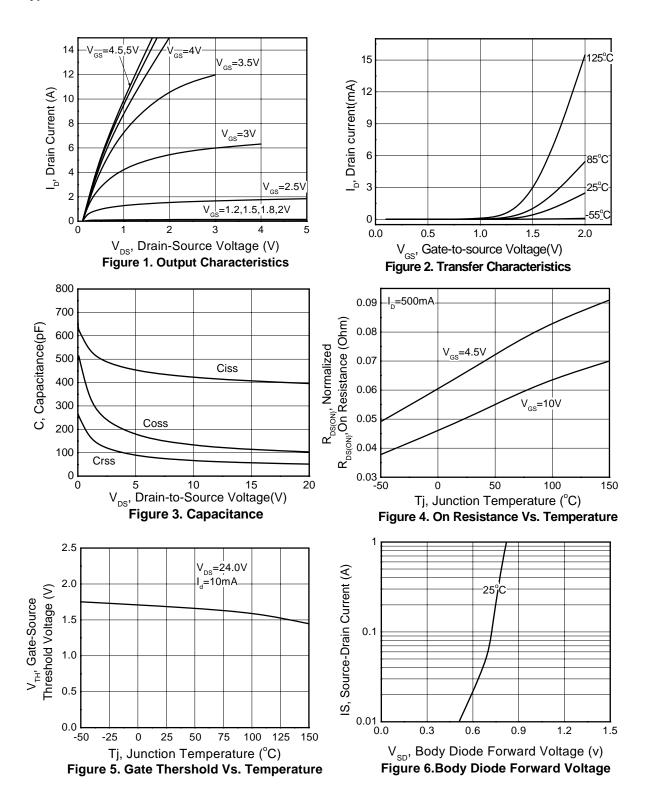
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit			
OFF CHARACTERISTICS									
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu \text{ A}$	-30	-36		٧			
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -24 V, V _{GS} = 0 V		0.02	-1	μА			
Gate-Body Leakage Current	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$		±1.5	±100	nA			
ON CHARACTERISTICS									
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1	-1.46	-3	V			
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = -10 V, I _D = -4.6 A		51	60	m Ω			
		V _{GS} = -4.5 V, I _D = -2 A		68	82				
Forward Transconductance	G _{FS}	V _{DS} = -5 V, I _D = -6 A		12		S			
DYNAMIC CHARACTERISTICS									
Input Capacitance	C _{ISS}	V _{DS} = -15 V, V _{GS} = 0 V, _f = 1.0 MHz		550		pF			
Output Capacitance	Coss			60					
Reverse Transfer Capacitance	C _{RSS}	- 1.0 WH 12		50					
SWITCHING CHARACTERISTICS									
Turn-On Delay Time	$T_{D(ON)}$	$V_{DS} = -15 \text{ V}, R_L = 2.5 \Omega,$ $V_{GS} = -10 \text{V}, R_{GEN} = 3 \Omega$		8.6		nS			
Turn-Off Delay Tim	$T_{D(OFF)}$			28.2					
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS									
Diode Forward Voltage	V _{SD}	V _{GS} = 0 V, I _S = -1 A		-0.81		V			

Note: 1. The value of P_D is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any given application depends on the user's specific board design. The current rating is based on the DC thermal resistance rating.

2. Repetitive rating, pulse width limited by junction temperature.



Typical Performance Characteristics





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