# MA3S132D (MA132WA), MA3S132E (MA132WK)

## Silicon epitaxial planar type

For switching circuits

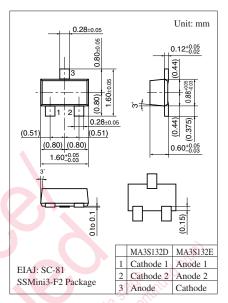
#### ■ Features

- Short reverse recovery time t<sub>rr</sub>
- Small terminal capacitance C<sub>t</sub>
- Two isolated elements contained in one package, allowing highdensity mounting

## ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter		Symbol	Rating	Unit	
Reverse voltage		V <sub>R</sub>	80	V	
Maximum peak reverse voltage		V <sub>RM</sub>	80	V	
Forward current	Single	$I_F$	100	mA	
	Double		150		
Peak forward	Single	$I_{FM}$	225	mA	
current	Double		340		
Non-repetitive peak	Single	I <sub>FSM</sub>	500	mA	
forward surge current *	Double		750		
Junction temperature		$T_{j}$	150	°C	
Storage temperature		T <sub>stg</sub>	-55 to +150	°C	

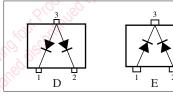
Note) \*: t = 1 s



#### Marking Symbol:

MA3S132D: MO
 MA3S132E: MU

#### Internal Connection

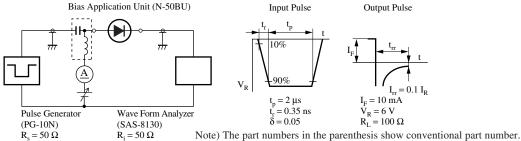


### ■ Electrical Characteristics $T_a = 25$ °C $\pm 3$ °C

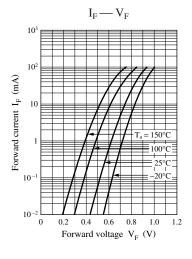
Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage		$V_{\hat{F}}$	$I_F = 100 \text{ mA}$			1.2	V
Reverse voltage		$V_R$	$I_R = 100 \mu A$	80			V
Reverse current		$I_R$	$V_R = 75 \text{ V}$			100	nA
Terminal capacitance	MA3S132D	$C_{t}$	$V_R = 0 \text{ V, f} = 1 \text{ MHz}$			15	pF
	MA3S132E	intell of				2	
Reverse recovery time *	MA3S132D	$t_{rr}$	$I_F = 10 \text{ mA}, V_R = 6 \text{ V}$			10	ns
	MA3S132E	Mall	$I_{rr} = 0.1 \; I_R , \; R_L = 100 \; \Omega$			3	

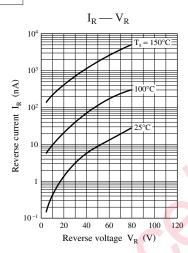
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

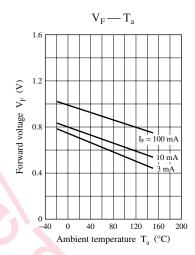
- 2. Absolute frequency of input and output is 100 MHz.
- 3. \*:  $t_{rr}$  measurement circuit

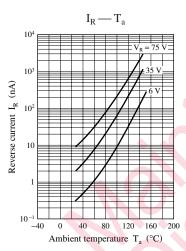


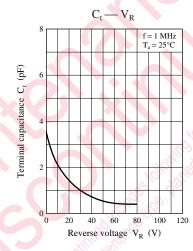
#### Characteristics charts of MA3S132D



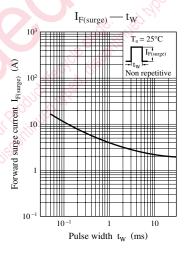




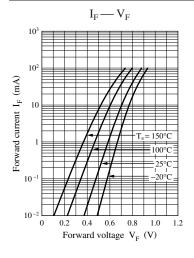


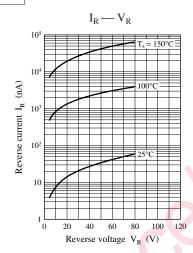


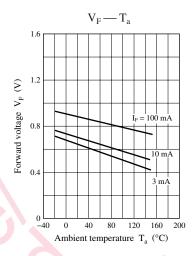
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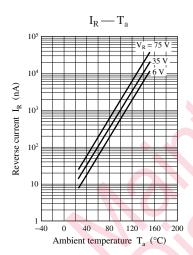


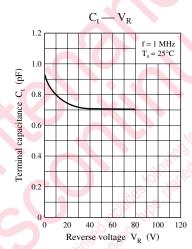
#### Characteristics charts of MA3S132E

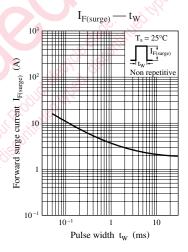












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