TOSHIBA Transistor Silicon NPN Triple Diffused Type

2SC5122

High-Voltage switching Applications

Unit: mm

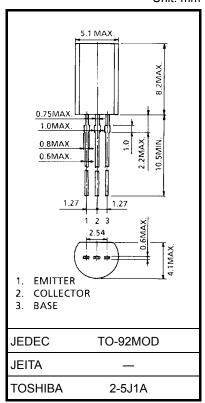
• High breakdown voltage: VCEO = 400 V

• Low saturation voltage: $V_{CE (sat)} = 0.4 \text{ V (typ.)}$ (I_C = 20 mA, I_B = 0.5 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V_{CBO}	400	V	
Collector-emitter voltage		V _{CEO}	400	V	
Emitter-base voltage		V _{EBO}	7	V	
Collector current	DC	IC	50	mA	
	Pulse	I _{CP}	100		
Base current		ΙΒ	25	mA	
Collector power dissipation		PC	900	mW	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	−55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.



Weight: 0.36 g (typ.)

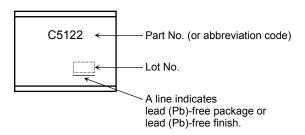
operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

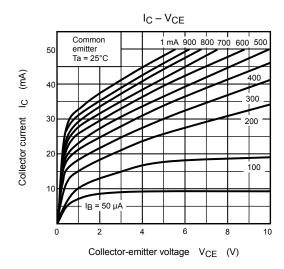


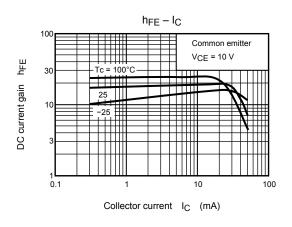
Electrical Characteristics (Ta = 25°C)

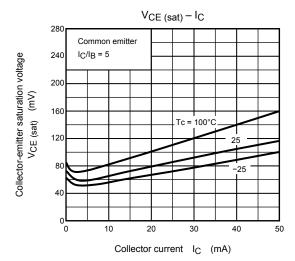
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	V _{CB} = 400 V, I _E = 0	_	_	1	μA
Emitter cut-off current	I _{EBO}	V _{EB} = 7 V, I _C = 0	_	_	1	μA
Collector-emitter breakdown voltage	V (BR) CEO	I _C = 1 mA, I _B = 0	400	_	_	٧
DC current gain	h _{FE (1)}	V _{CE} = 5 V, I _C = 1 mA	80	_	_	
	h _{FE (2)}	V _{CE} = 5 V, I _C = 20 mA	100	_	300	
Collector-emitter saturation voltage	V _{CE} (sat)	I _C = 20 mA, I _B = 0.5 mA	_	0.4	1.0	٧
Base-emitter voltage	V_{BE}	V _{CE} = 5 V, I _C = 20 mA	_	0.7	1.0	V
Collector output capacitance	C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	4	_	pF

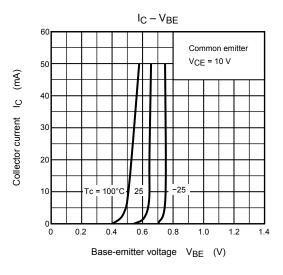
Marking

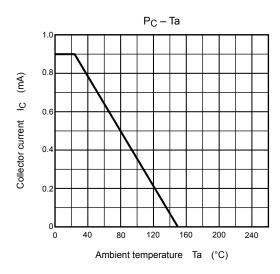


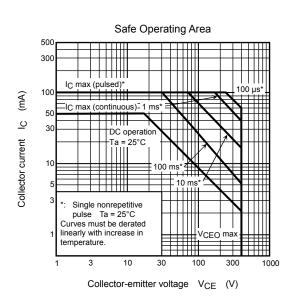












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