Watchdog Timer

HITACHI

Description

The HA1835P and HA1848P is a monolithic voltage regulator control designed for microcomputer systems. In addition to the voltage regulator, it include watchdog timer function and power-on reset function.

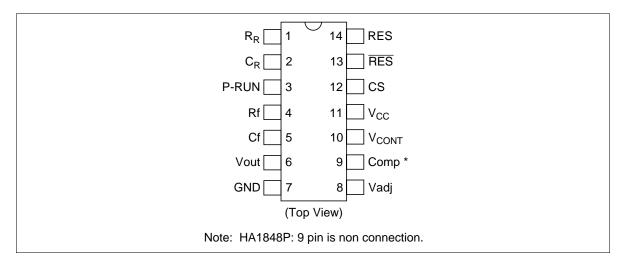
These ICs can perform many function in various microcomputer systems with few external parts.

Features

- Regulated power supply control function
 - Wide area of operational voltage; $V_{CC} = 6 \text{ V}$ to 30 V
 - Available external PNP-type transistor, suited to any system power supply
 - Built-in overcurrent limiter for external PNP-type transistor
 - Output voltage can be adjusted exactry in the range from 4 V to 6 V
- Watchdog timer
 - Internal bandpass filter control circuit (pulse width detect type) and reset signal oscillator
 - Fail-safe utility
 - Bandpass filter characteristics can be set by external resister (Rf) and capacitance (Cf)
- Automatic reset
 - Automatic power-on reset
 - Pulse generator characteristics can be set by external resistor (R_R) and capacitance (C_R)
 - Alternative between RES and RES output



Pin Arrangement

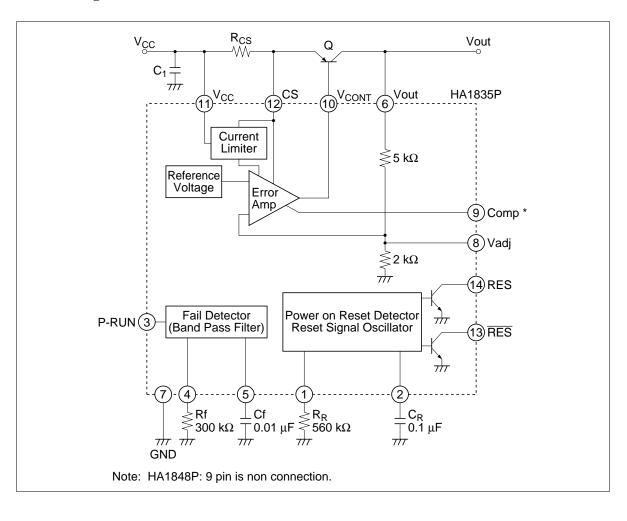


Pin Functions

Pin No.	Symbol	Functions
1	R_R	Reset pulse width depends on resistance connected to $R_{_R}$ Recommended range: 100 $k\Omega$ to 1 $M\Omega$
2	C_R	Reset pulse width depends on capacitance connected to C _R
3	P-RUN	Clock pulse input terminal for watchdog timer
4	Rf	Frequency band width of filter circuit depends on resistance connected to Rf Recommended range: 100 k Ω to 500 k Ω
5	Cf	Frequency band width of filter circuit depends on capacitance connected to Cf
6	Vout	Connect to external PNP transistor's collector: This pin supplies 5 V regulated voltage for internal circuit
7	GND	Ground
8	Vadj	Output voltage fine tuning terminal
9	Comp	Phase compensation terminal: Connect less than 100 pF capacitor between $\rm V_{cc}$ and Comp. HA1848P is non connection
10	V _{CONT}	External PNP transistor's base control terminal
11	V _{cc}	Supply voltage terminal: Operating supply voltage range is 6 V to 30 V
12	CS	Connect current sense resistor, which protects the external PNP transistor, between $V_{\rm cc}$ and CS
13	RES *	Reset pulse output terminal for low level reset type microcomputer
14	RES *	Reset pulse output terminal for high level reset type microcomputer
		

Note: $\overline{\text{RES}}$ and RES are open-collector output terminals, so connect a pull-up resistor of about 5 k Ω .

Block Diagram



Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

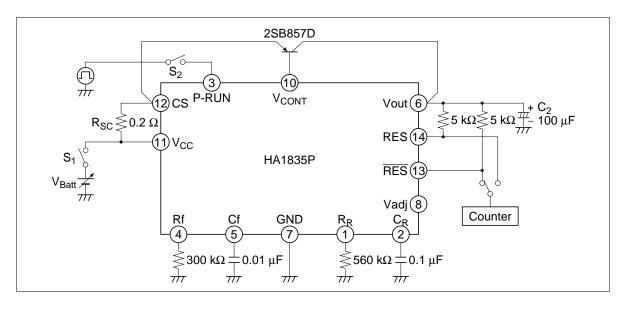
Item	Symbol	Rating	Unit	
Supply voltage	V _{cc}	30	V	
P-RUN input voltage	V_{P-RUN}	–0.3 to Vout	V	
Output voltage	V_{RES}	17.5	V	
	V _{RES}	17.5	V	
Output current	I _{RES}	2	mA	
	IRES	2	mA	
Control terminal voltage	V_{CONT}	V _{cc}	V	
Control terminal current	I _{CONT}	20	mA	
Power dissipation (Note)	P _T *	400	mW	
Operating temperature range	Topr	-40 to +85	°C	
Storage temperature range	Tstg	-50 to +125	°C	
Soldering temperature	Tsol	+260 (< 10 sec)	°C	_

Note: $Ta \le 77^{\circ}C$. If $Ta > 77^{\circ}C$, derate by 8.3 mW/°C.

Electrical Characteristics ($V_{CC} = 12~V,~Vout = 5~V,~Ta = 25^{\circ}C$)

Item		Symbol	Min	Тур	Max	Unit	Test Conditions
Power supply terminal current		I _{cc}	_	6.3	12	mA	V _{CC} = 17.5 V (No load), with PNP transistor
Regulator	Output voltage	Vout1	4.75	5.00	5.25	V	$V_{CC} = 6 \text{ to } 17.5 \text{ V, lout} = 0.5 \text{ A}$
section		Vout2	4.70	5.00	5.30	V	$V_{CC} = 6 \text{ to } 17.5 \text{ V, lout} = 1 \text{ A}$
	Line regulation	δV_{Oline}	-50	_	+50	mV	$V_{CC} = 6 \text{ to } 17.5 \text{ V, lout} = 1 \text{ A}$
	Load regulation	δV_{Oload}	-100	_	+100	mV	lout = 10 mA to 0.5 A
	Ripple rejection	R _{REJ}	40	75	_	dB	$e_i = 0.5 \text{ Vrms}, f_i = 1 \text{ kHz}$
	Limiter operating current	I _{cs}	1.0	_	2.0	Α	$R_{SC} = 0.2 \Omega$
	Output voltage temperature coefficient	δVout/δT	_	-0.6	_	mV/°C	
P-RUN input	Low-level input voltage	V _{IL}	_	_	8.0	V	
section	High-level input voltage	V _{IH}	2.0	_	_	V	
	Low-level input current	I _{IL}	-120	-60	_	μΑ	V _{IL} = 0 V
	High-level input current	I _{IH}	_	1.8	3.0	mA	Vout = 5 V, V _{IH} = 5 V
Reset circuit section	Reset terminal low-level voltage	V _{OL1}	_	_	0.4	V	I _{OL} = 2 mA
	Reset terminal	I _{OH1}	_	_	5.0	μΑ	V _{OH} = 5 V
	leakage current	I _{OH3}	_	_	30	μΑ	V _{OH} = 17.5 V
Reset time	Power on time	t _{on}	80	130	200	ms	Rf = 300 k Ω , R _R = 560 k Ω ,
	Clock off reset time	t _{off}	60	130	220	ms	Cf = 0.01 μ F, C _R = 0.1 μ F
	Reset pulse low-level time	t _{RL}	40	80	160	ms	-
	Reset pulse high-level time	t _{RH}	50	100	200	ms	=

Test Circuit



Functional Description

Voltage Regulator

The HA1835P, and HA1848P supplies 5 V regulated output from a wide $V_{\rm CC}$ input range ($V_{\rm CC} = 6$ V to 17.5 V). The external PNP transistor should be selected according to the supply current demand of the system. Connect a more than 100 μ F capacitor between Vout and GND to realize the 5 V regulated output.

The resistor connected from the Vadj terminal to the Vout or GND terminal fine tunes the output voltage. A resistor between Vadj and the Vout decreases the output voltage, and a resistor between Vadj and GND increases the output voltage.

R (Vout – Vadj)
$$\approx \frac{5 \cdot \text{Vout} - 7.14}{5 - \text{Vout}}$$
 (k\O)

$$R \text{ (Vadj-GND)} \approx \frac{7.14}{\text{Vout} - 5} \quad \text{(k}\Omega\text{)}$$

Current Limiter

To protect the external transistor from overcurrent, connect a current-sense resistor between the CS terminal and the $V_{\rm CC}$ terminal. The value of this resistor should be:

$$R_{SC} \approx \frac{0.3 \text{ V}}{\text{lout (Limit)}}$$
 (at Ta = 25°C)

Power-On Reset

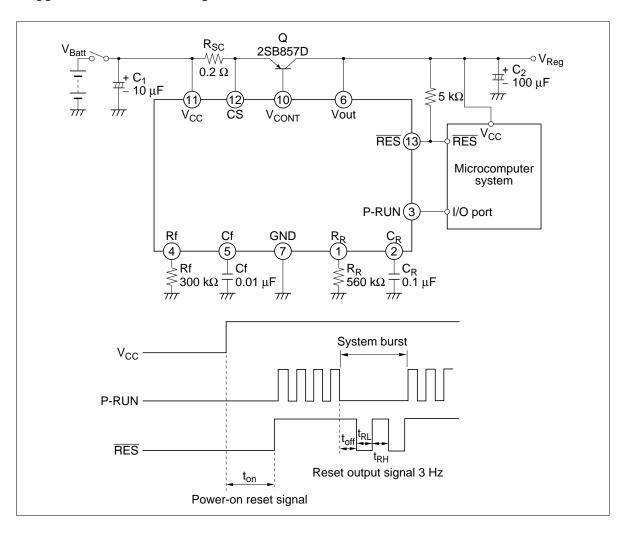
The HA1835P, and HA1848P can output a reset pulse to start the microcomputer at power on (figure 6). The external resistor R_R and capacitor C_R determine the power-on reset timing.

Note that \overline{RES} and RES are open-collector terminals. They should therefore be connected to a pull-up resistor of about 5 k Ω .

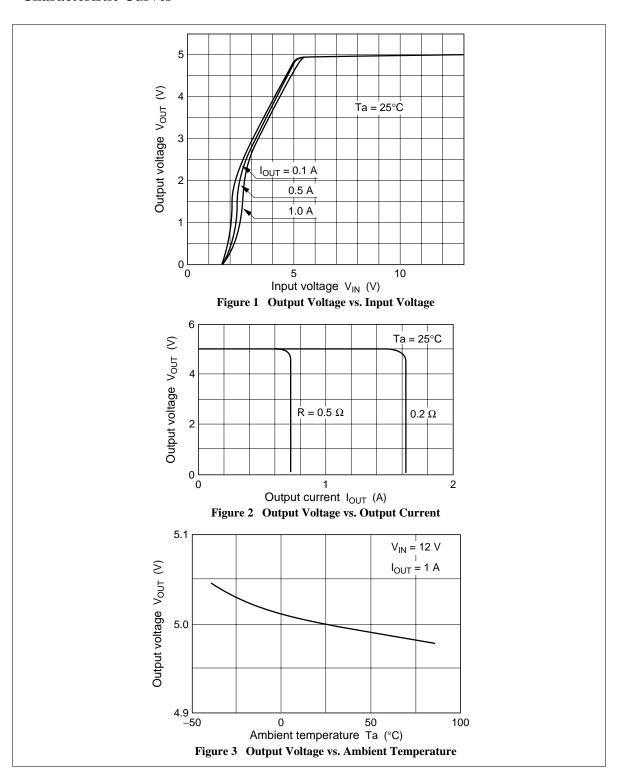
Watchdog Timer

The watchdog timer is a fail-safe function. It can reset the microcomputer system if the system runs out of control. It does this be monitoring a pulse output by the system's software. It uses a bandpass filter to determine whether the pulse frequency is within the system's normal frequency band (figure 7, 8). External resistor and capacitor Rf and Cf determine the frequency range of the bandpass filter. If the pulse frequency is not within the frequency band, the HA1835P, and HA1848P outputs a reset pulse.

Application Circuit Example



Characteristic Curves



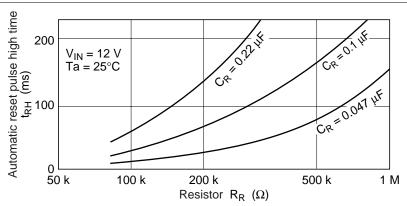


Figure 4 Automatic Reset Pulse High Time vs. Resistor

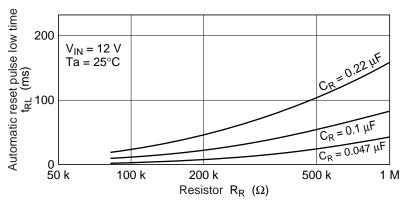
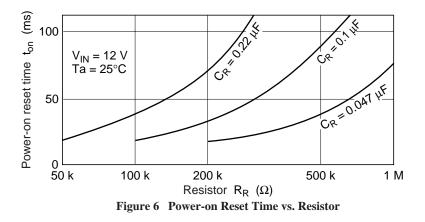


Figure 5 Automatic Reset Pulse Low Time vs. Resistor



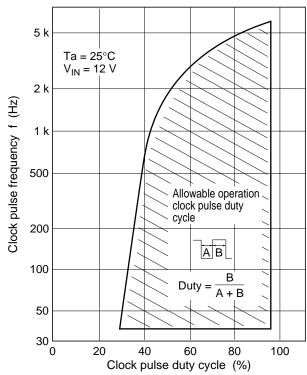
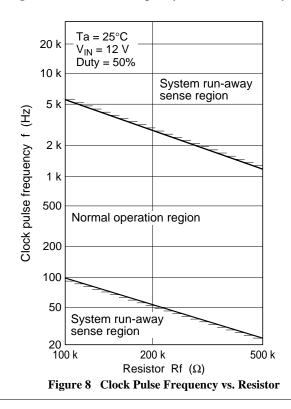
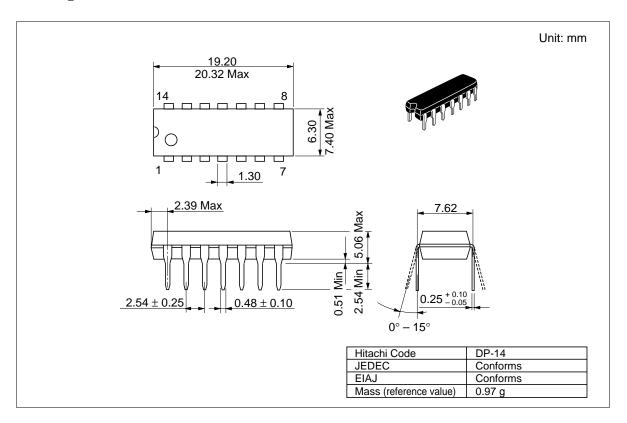


Figure 7 Clock Pulse Frequency vs. Clock Pulse Duty Cycle



Package Dimensions



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