



# SP6018

## Synchronous Rectifier Driver

Rev	Date	ECN No.	Author	Description
1.0				Original
2.0	08/29/2012	PEN-1208-003	Sandra Hu	1.Change Package outline Drawing.
3.0	10/29/2012		Sandra Hu	1.Modify Iout from 1A to 2A.
4.0	12/05/2012	PEN-1212-002	Sandra Hu	1.Modify Pin Description. 2.Updatd Vdd on and Vslth specificaltion. 3.Add VMOS-G.VR.Vtiming. 4.Modify Waveform and Performance Characteristics( Figure1,2,3,5 and 6).
Title				
SP6018 Datasheet				



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## Synchronous Rectifier Driver

### DESCRIPTION

The fundamental of SP6018 synchronous rectifier (SR) driver IC is based on our U.S. patented methods that utilize the principle of “prediction” logic circuit. The IC deliberates previous cycle timing to control the SR in present cycle by “predictive” algorithm that makes adjustments to the turn-off time, in order to achieve maximum efficiency and avoid cross-conduction at the same time. Specially, SP6018 is designed for Resonance. It also maintains the MOSFET’s body diode conduction at minimum level. The SP6018 is capable to adapt in almost all existing Resonance converters with few adjustments considered necessary.

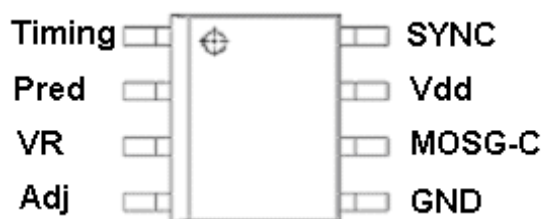
### FEATURES

- Offers efficiency improvement over Schottky Diode (depends on drive configuration of the SR).
- Drives all logic level Power MOSFET.
- Prediction gate timing control.
- Minimum MOSFET body diode conduction.
- Operating frequency up to 400 KHz.
- Synchronize to transformer secondary voltage waveform.

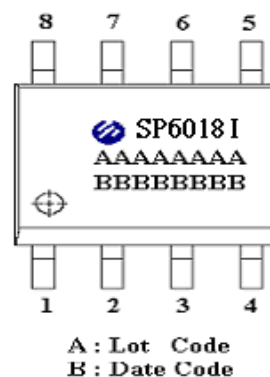
### APPLICATIONS

- Servers & workstations
- Storage area network power supplies
- Telecommunication converters
- Embedded systems
- Industrial & commercial systems using high current processors

### PIN CONFIGURATION (SOP-8)



### PART MARKING

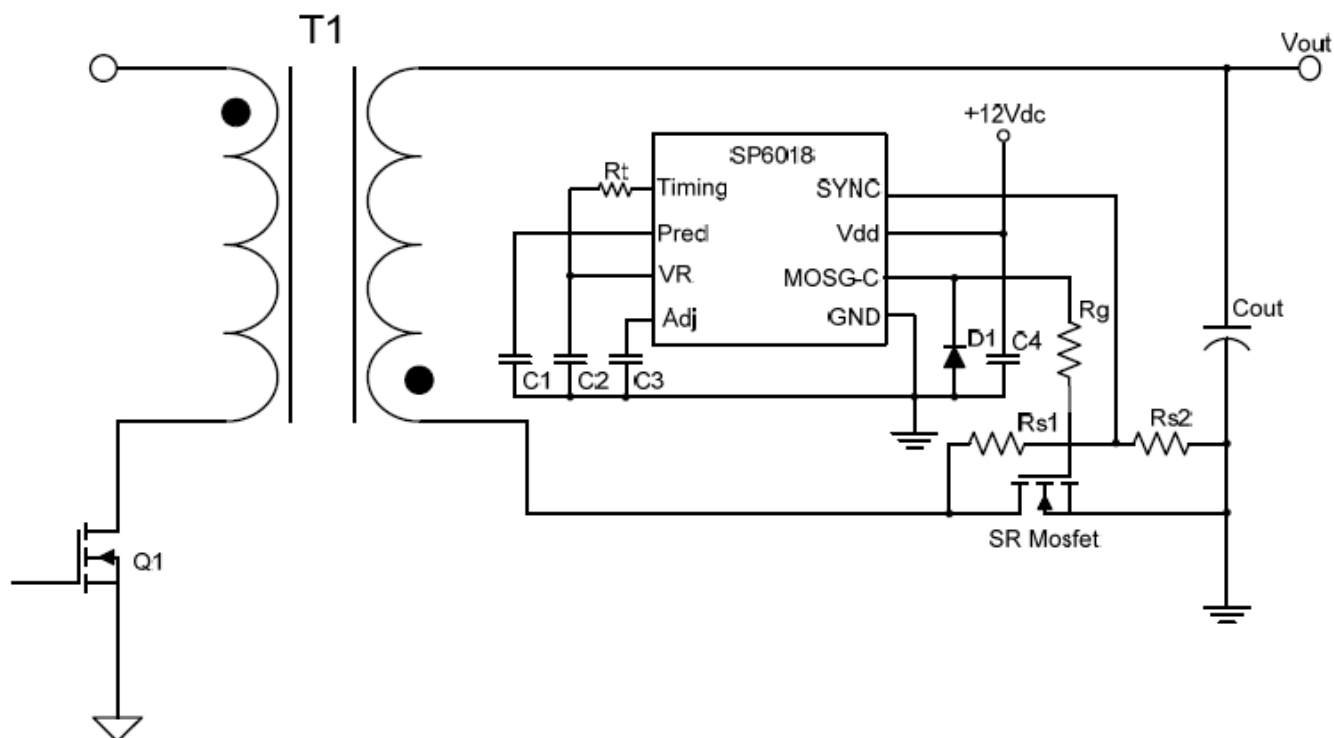




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### TYPICAL APPLICATION CIRCUIT



### PIN DESCRIPTION

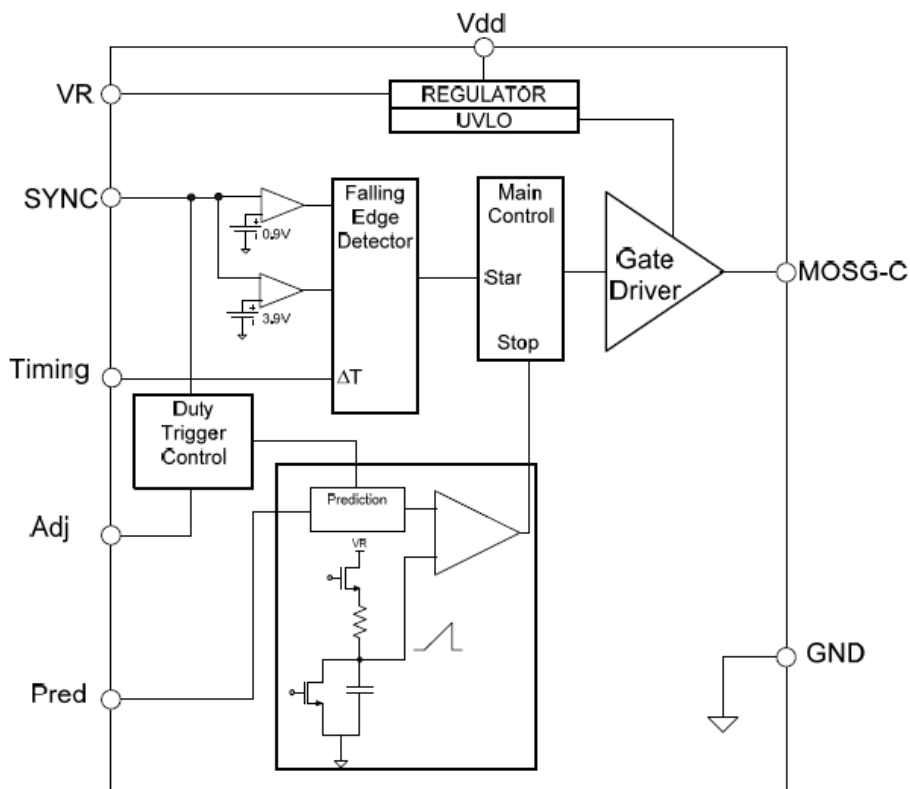
Pin	Symbol	Description
1	Timing	Discontinuous current filter timing adjustment resistor connection.
2	Pred	Capacitor to store previous cycle timing for SR MOSFET.
3	VR	Voltage Regulator.
4	Adj	Trigger point adjustment for Dynamic state.
5	GND	Ground connection.
6	MOSG-C	Catch MOSFET gate drive.
7	Vdd	DC supply voltage.
8	SYNC	Synchronized signal from the $V_{DS}$ of SR MOSFET.



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### BLOCK DIAGRAM



### ORDERING INFORMATION

Part Number	Package	Part Marking
SP6018S8RGB	SOP-8	SP6018I
SP6018S8TGB	SOP-8	SP6018I

※ SP6018S8RGB : Tape Reel ; Pb – Free ; Halogen - Free

※ SP6018S8TGB : Tube ; Pb – Free ; Halogen - Free

### ABSOLUTE MAXIMUM RATINGS (TA=25°C , unless otherwise specified.)

The following ratings designate persistent limits beyond which damage to the device may occur.

Symbol	Parameter	Value	Unit
V <sub>dd</sub>	DC Supply Voltage	16	V
V <sub>MOS-G</sub>	Output Voltage	16	V
V <sub>R</sub>	Voltage Regulator	-0.3~8	V
V <sub>Timing/pred/Adj/sync</sub>	Timing/Pred/Adj/Sync Voltage	-0.3~6	V
I <sub>OUT</sub>	Peak Source Current (Pulsed)	2.0	A
	Peak Sink Current (Pulsed)	2.0	A
P <sub>D</sub>	Power Dissipation @ T <sub>A</sub> =85°C (*)	0.25	W
T <sub>J</sub>	Operating Junction Temperature Range	-40 to 125	°C
T <sub>STG</sub>	Storage Temperature Range	-40 to 150	°C
T <sub>LEAD</sub>	Lead Soldering Temperature for 5 sec.	260	°C



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### THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance Junction – Case (*)	45	$^{\circ}\text{C}/\text{W}$

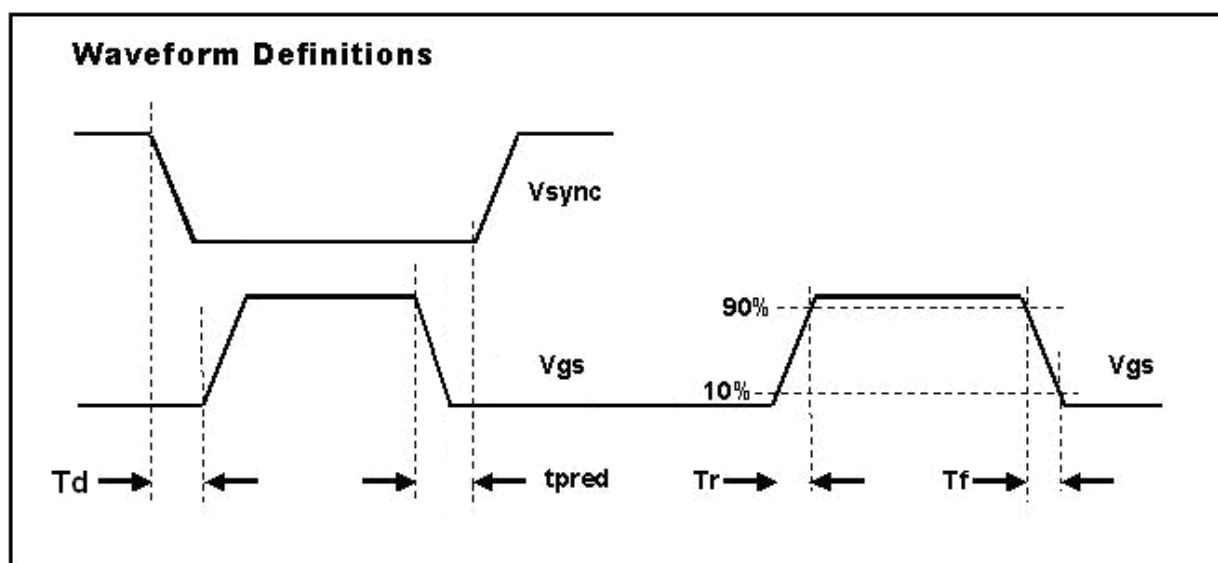
(\*) The power dissipation and thermal resistance are evaluated under copper board mounted with free air conditions.

### ELECTRICAL CHARACTERISTICS

( $T_A=25^{\circ}\text{C}$ ,  $V_{dd}=12\text{V}$ , Freq. =300 KHz, Duty Cycle=50%, unless otherwise specified.)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>SUPPLY INPUT</b>						
IDD	Supply current	No load		4	7	mA
		$V_{\text{SYNC}}=0\text{V}$ , No load		5	8	mA
Vdd	Supply voltage	$I_{\text{dd peak}} < 2\text{A}$			16	V
Vdd on	Enable voltage		9.4	10.0	10.5	V
<b>SYNC REFERENCE (SYNC)</b>						
Vshth	SYNC high threshold			3.9		V
Vslth	SYNC low threshold			0.9		V
Vsync	SYNC clamp voltage	$I_{\text{sync}}=3\text{mA}$		5		
Isync	SYNC input current				3	mA
<b>Voltage Regulator REFERENCE (VR)</b>						
IvR	VR Output Current				20	mA
<b>ON TIME DUTY SETUP ( PIN 6 )</b>						
Ton-time				20		us
<b>MOSFET GATE DRIVER (MOSG-C)</b>						
Voh	Output high voltage	$I_o = -200\text{mA}$	10.5	11.0		V
Vol	Output low voltage	$I_o = 200\text{mA}$		0.5	0.8	V
Td	Propagation delay	No load	50	80		ns
Tpred		No load		120		ns
Tr	Rise time	Load = 1nF (*)		10	25	ns
Tf	Fall time	Load = 1nF (*)		10	25	ns
<b>Dynamic Protect</b>						
Dt	Dynamic variable	Pin 4 open		600		ns
Ton-min	MOSG-C on time	PWM adjusts time > Dt		1		us

(\*)  $T_r$  &  $T_f$  are measured among 10% and 90% of starting and final voltage.





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PERFORMANCE CHARACTERISTICS ( $T_A=25^\circ\text{C}$ , unless otherwise specified.)

Figure 1 : Supply Current vs Supply Voltage

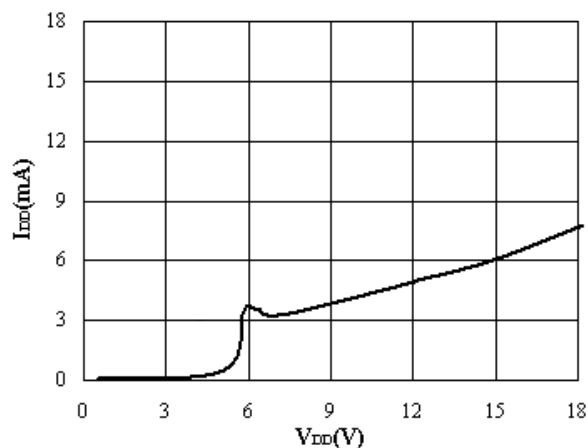


Figure 2 : Supply Current vs Freq. @ No Load

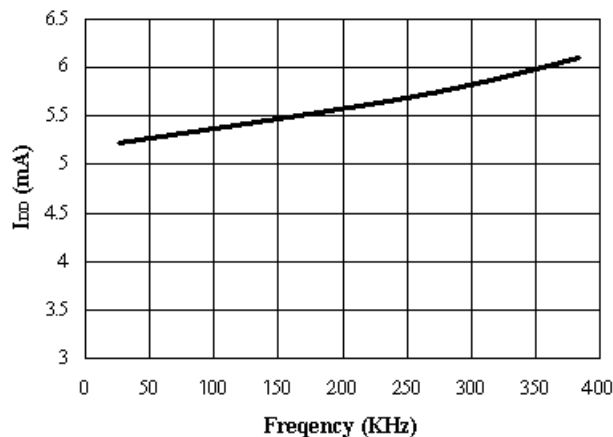


Figure 3 : T<sub>pred</sub> vs C<sub>pred</sub> @ Freq=100 KHz ; V<sub>DD</sub>=10V

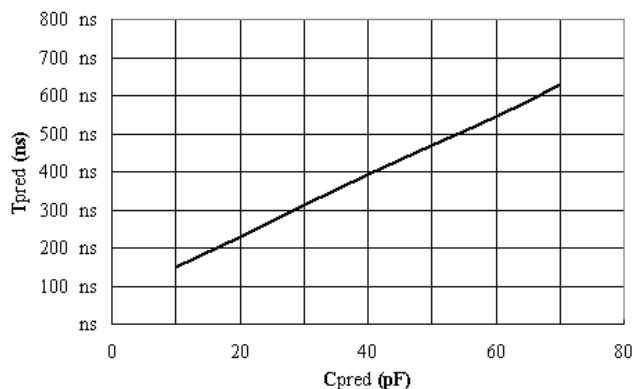


Figure 4 : Output Rise Time vs Load Capacitor

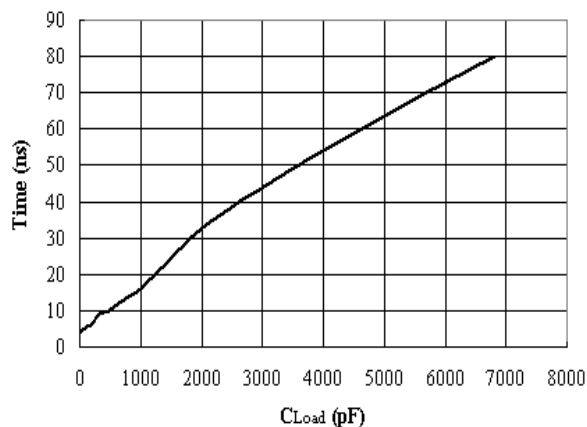


Figure 5 : Output Fall Time vs Load Capacitor

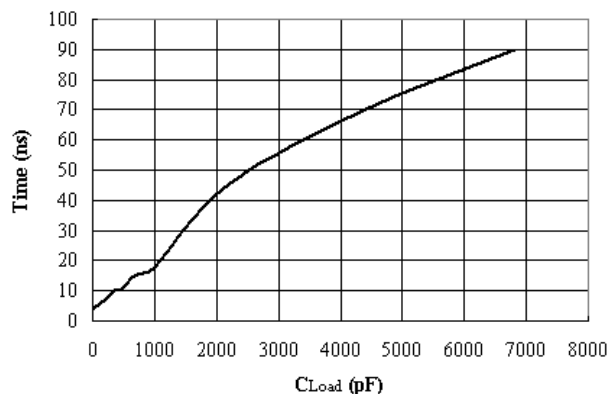
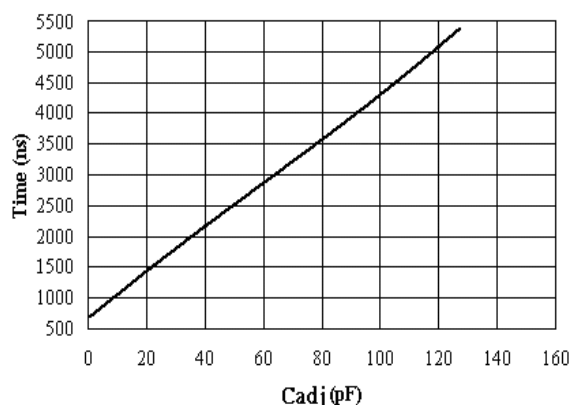


Figure 6 : Dynamic time vs Load Capacitor



\*Fig. 1 : No Load ; No SYNC

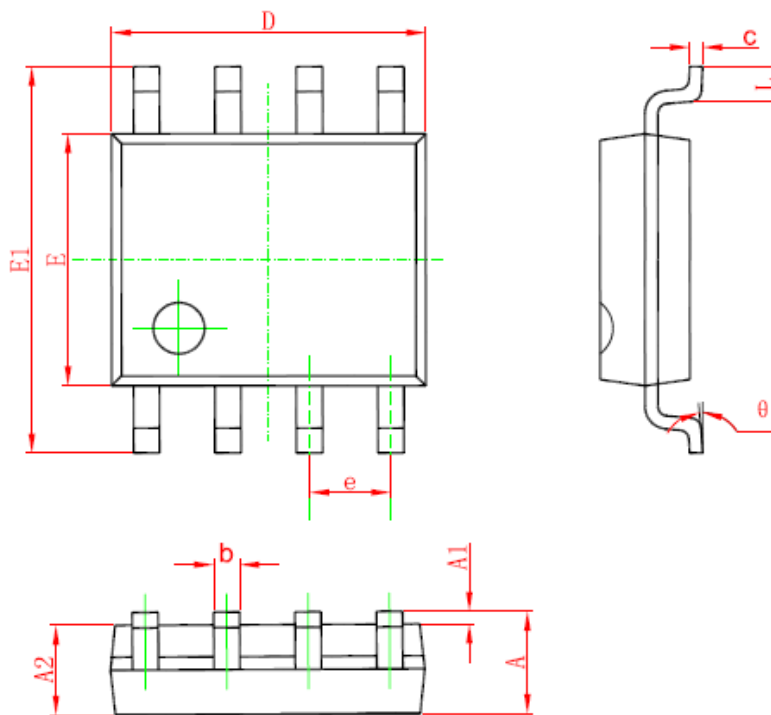
\*Fig. 4~5 : Frequency = 65 kHz.



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### SOP- 8 PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



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