TOSHIBA Field Effect Transistor Silicon P Channel MOS Type ($L^2-\pi$ -MOSV)

2SJ402

DC–DC Converter, Relay Drive and Motor Drive Applications

- 4 V gate drive
- Low drain-source ON resistance $: R_{DS} (ON) = 29 \text{ m}\Omega (typ.)$
- High forward transfer admittance $: |Y_{fs}| = 23 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = -100 \ \mu A \ (max) \ (V_{DS} = -60 \ V)$
- Enhancement-mode : $V_{th} = -0.8 \sim -2.0 V (V_{DS} = -10 V, I_D = -1 mA)$

Maximum Ratings (Ta = 25°C)

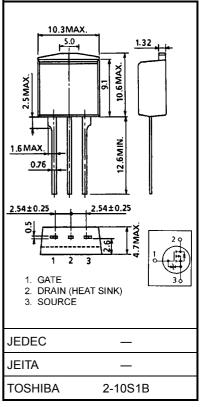
Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	-60	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	-60	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	۱ _D	-30	A	
	Pulse(Note 1)	I _{DP}	-120	А	
Drain power dissipation	n (Tc = 25°C)	PD	100	W	
Single pulse avalanche energy (Note 2)		E _{AS}	936	mJ	
Avalanche current		I _{AR}	-30	А	
Repetitive avalenche energy (Note 3)		E _{AR}	10	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55~150	°C	

Thermal Characteristics

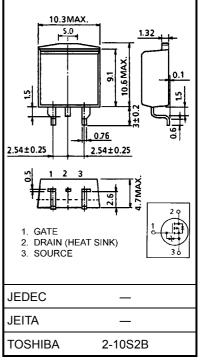
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.25	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	83.3	°C / W

- Note 1: Please use devices on condition that the channel temperature is below 150°C.
- Note 2: V_{DD} = -50 V, T_{ch} = 25°C (initial), L = 747 μ H, R_G = 25 Ω, I_{AR} = -30 A
- Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



Weight: 1.5 g (typ.)



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Unit: mm

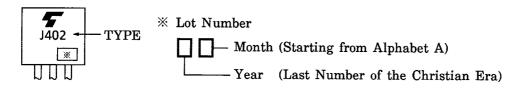
Electrical Characteristics (Ta = 25°C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V		_	±10	μA
Drain cut-off cu	rrent	IDSS	V_{DS} = -60 V, V_{GS} = 0 V	_		-100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = -10 mA, V _{GS} = 0 V	-60	_	_	V
Gate threshold v	voltage	V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-0.8		-2.0	V
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = -4 V, I _D = -15 A		46	60	mΩ
			V _{GS} = -10 V, I _D = -15 A	_	29	38	11122
Forward transfer	admittance	Y _{fs}	V _{DS} = -10 V, I _D = -15 A	14	23	—	S
Input capacitance	e	C _{iss}		_	3300	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	460	_	pF
Output capacitance		Coss			1450	_	
Switching time	Rise time	tr	$V_{GS} \xrightarrow{0V}_{10V} \xrightarrow{I_D = -15A}_{V_{OUT}} \xrightarrow{V_{OUT}}_{RL =} \xrightarrow{2\Omega}_{V_{DD} = -30V}$	-	20	_	
	Turn-on time	t _{on}		_	25	_	20
	Fall time	t _f		_	35	_	- ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w =10 μ s	-	130	_	
Total gate charge (Gate-source plus gate-drain)		Qg	V _{DD} ≈ −48 V, V _{GS} = −10 V, I _D = −30 A		110	_	
Gate-source charge		Q _{gs}			75		nC
Gate-drain ("miller") charge		Q _{gd}			35		

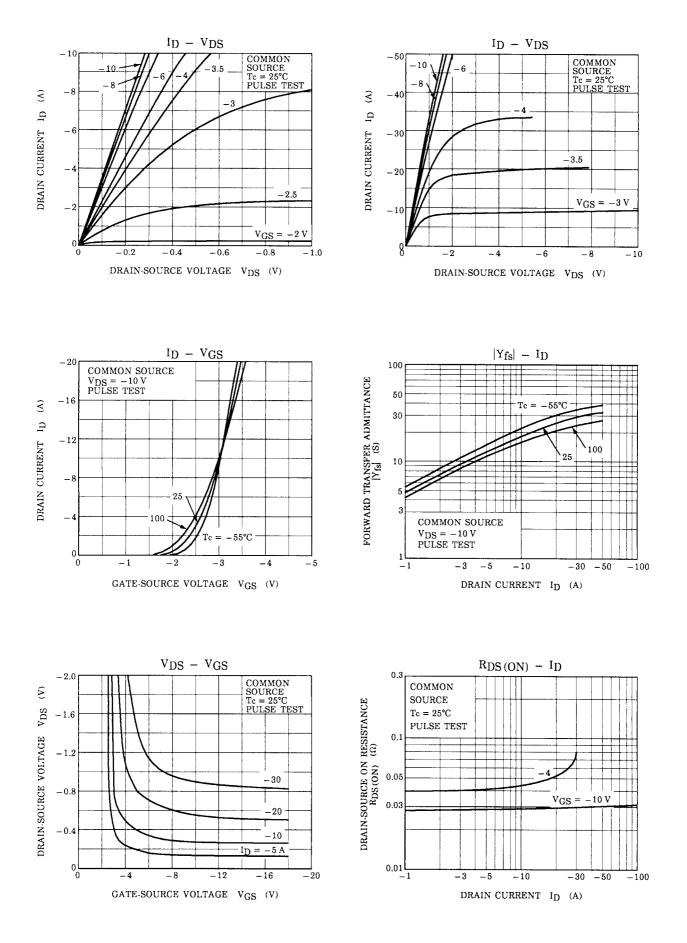
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—		_	-30	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	-120	А
Forward voltage (diode)	V _{DSF}	I _{DR} = -30 A, V _{GS} = 0 V	_	_	1.7	V
Reverse recovery time	t _{rr}	I _{DR} = −30 A, V _{GS} = 0 V	_	100		ns
Reverse recovery charge	Qrr	dI_{DR} / $dt = 50 \text{ A}$ / μ S	_	0.16	_	μC

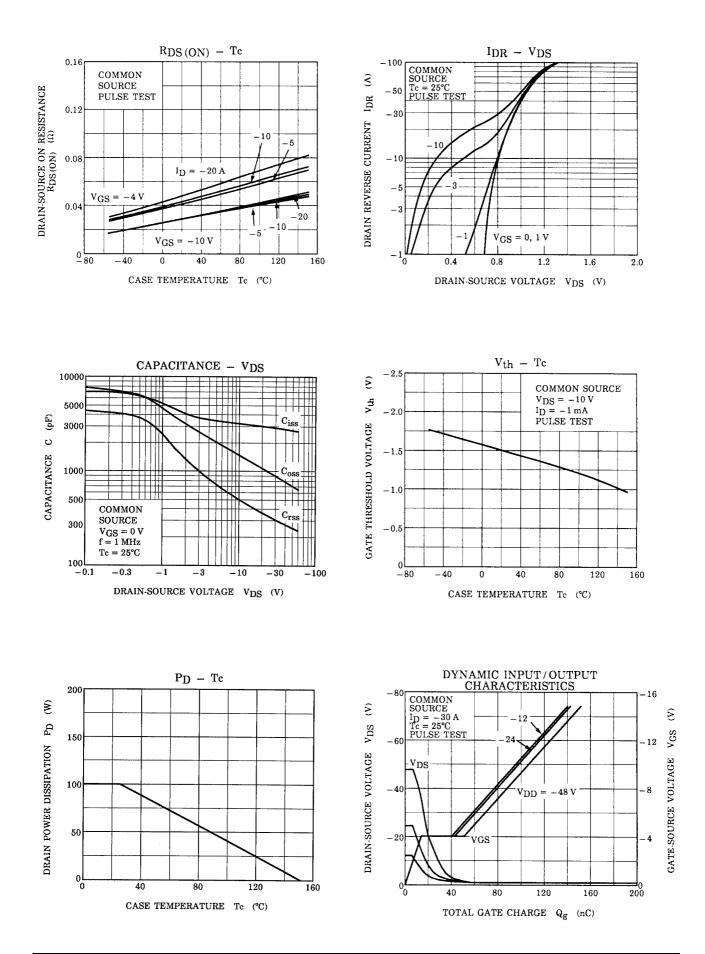
Marking

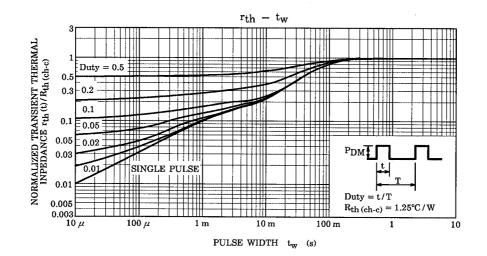


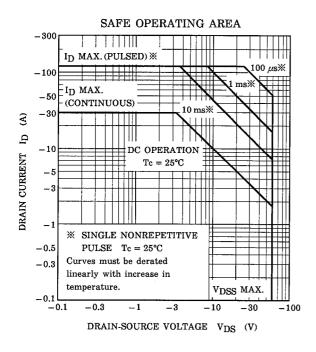
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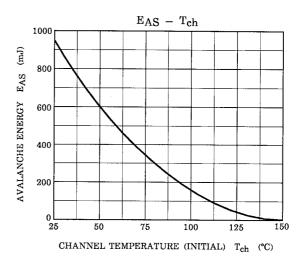


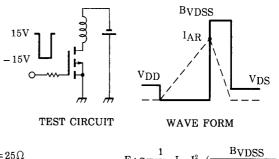
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 $R_G = 25\Omega$ $V_{DD} = -50V$, L=747 μ H

 $E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}}\right)$

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