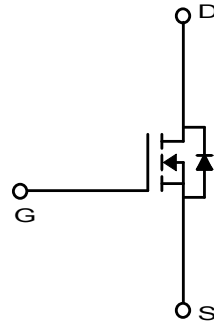
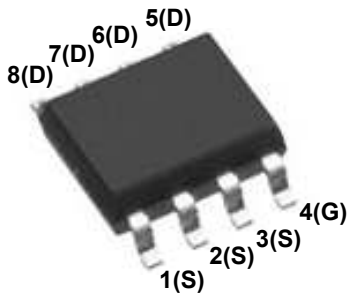


**General Description**

The MDS1525 uses advanced MagnaChip’s MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDS1525 is suitable for DC/DC converter and general purpose applications.

**Features**

- $V_{DS} = 30V$
- $I_D = 16.9A @V_{GS} = 10V$
- $R_{DS(ON)} < 10.1m\Omega @V_{GS} = 10V$   
 $< 14.9m\Omega @V_{GS} = 4.5V$
- 100% UIL Tested
- 100% Rg Tested



**Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		$V_{DSS}$	30	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current <sup>(1)</sup>	$T_C=25^\circ C$	$I_D$	16.9	A
	$T_C=70^\circ C$		13.6	
	$T_A=25^\circ C$		11.8 <sup>(3)</sup>	
	$T_A=70^\circ C$		9.5 <sup>(3)</sup>	
Pulsed Drain Current		$I_{DM}$	40	A
Power Dissipation	$T_C=25^\circ C$	$P_D$	5.1	W
	$T_C=70^\circ C$		3.3	
	$T_A=25^\circ C$		2.5 <sup>(3)</sup>	
	$T_A=70^\circ C$		1.6 <sup>(3)</sup>	
Single Pulse Avalanche Energy <sup>(2)</sup>		$E_{AS}$	43.6	mJ
Junction and Storage Temperature Range		$T_J, T_{stg}$	-55~150	°C

**Thermal Characteristics**

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	50	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	24.2	

## Ordering Information

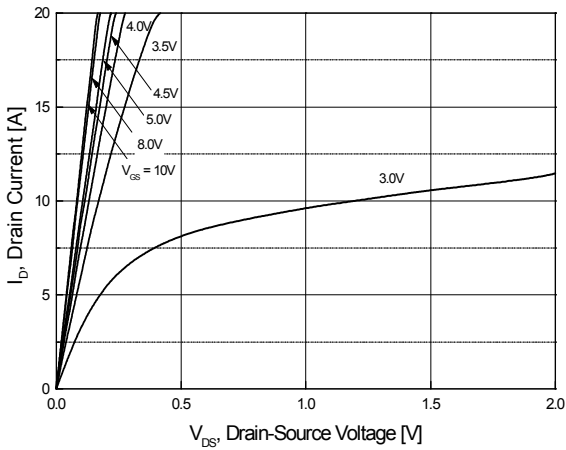
Part Number	Temp. Range	Package	Packing	Quantity	Rohs Status
MDS1525URH	-55~150°C	SOIC-8	Tape & Reel	3000 units	Halogen Free

## Electrical Characteristics (T<sub>J</sub> = 25°C)

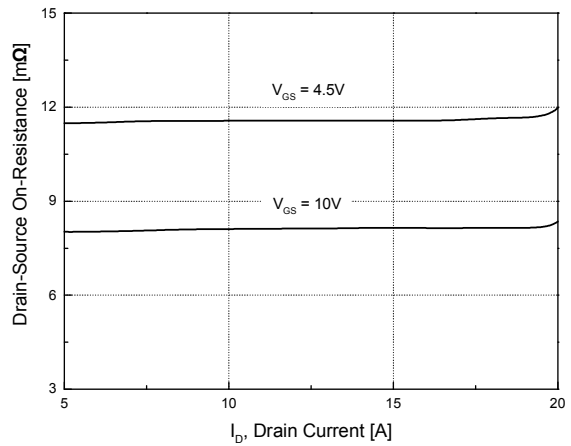
Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.3	1.9	2.7	
Drain Cut-Off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V T <sub>J</sub> =55°C	-	-	1 5	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	-	-	±0.1	
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 9A T <sub>J</sub> =125°C	-	8.8 12.8	10.1 14.6	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 7A	-	12.4	14.9	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 9A	-	27.8	-	S
<b>Dynamic Characteristics</b>						
Total Gate Charge	Q <sub>g(10V)</sub>	V <sub>DS</sub> = 15.0V, I <sub>D</sub> = 9A, V <sub>GS</sub> = 10V	9.1	13.0	16.9	nC
Total Gate Charge	Q <sub>g(4.5V)</sub>		4.3	6.2	8.1	
Gate-Source Charge	Q <sub>gs</sub>		-	2.3	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	2.0	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15.0V, V <sub>GS</sub> = 0V, f = 1.0MHz	554	792	1029	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		54	78	101	
Output Capacitance	C <sub>oss</sub>		108	154	200	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15.0V, I <sub>D</sub> = 9A, R <sub>G</sub> = 3.0Ω	-	5.8	-	ns
Rise Time	t <sub>r</sub>		-	10.9	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	21.1	-	
Fall Time	t <sub>f</sub>		-	7.3	-	
Gate Resistance	R <sub>g</sub>	f=1 MHz	0.5	1.4	3.0	Ω
<b>Drain-Source Body Diode Characteristics</b>						
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 9A, V <sub>GS</sub> = 0V	-	0.83	1.1	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 9A, di/dt = 100A/μs	-	21.9	32.8	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	13.0	19.5	nC

Note :

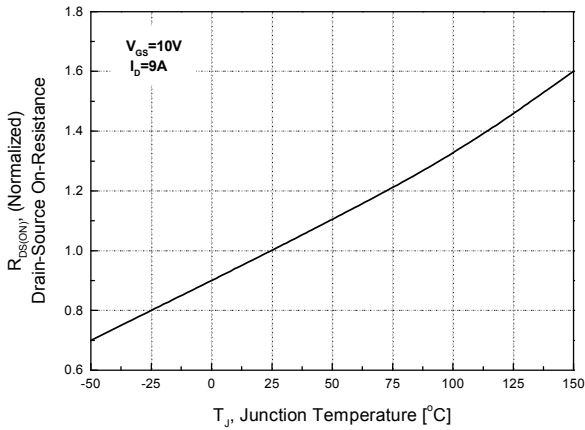
- Surface mounted FR-4 board by JEDEC (jesd51-7)
- E<sub>AS</sub> is tested at starting T<sub>J</sub> = 25°C, L = 0.1mH, I<sub>AS</sub> = 16.2A, V<sub>DD</sub> = 27V, V<sub>GS</sub> = 10V.
- T < 10sec.



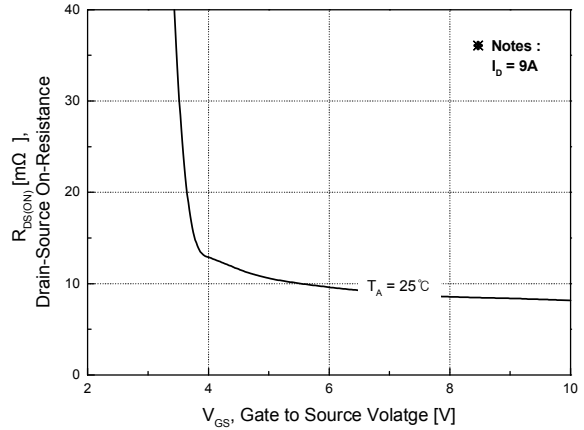
**Fig.1 On-Region Characteristics**



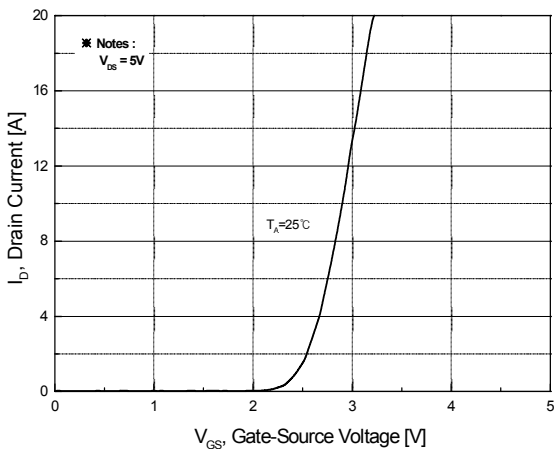
**Fig.2 On-Resistance Variation with Drain Current and Gate Voltage**



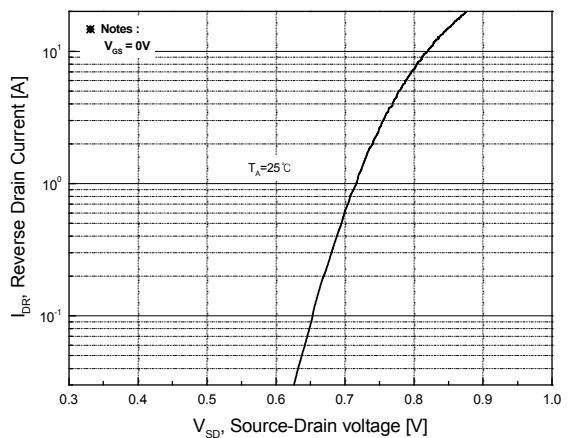
**Fig.3 On-Resistance Variation with Temperature**



**Fig.4 On-Resistance Variation with Gate to Source Voltage**



**Fig.5 Transfer Characteristics**



**Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature**

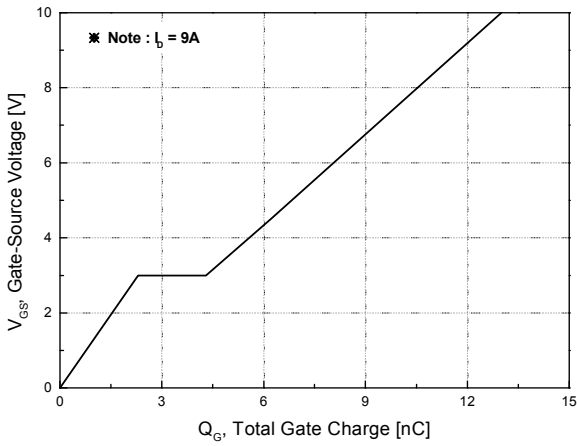


Fig.7 Gate Charge Characteristics

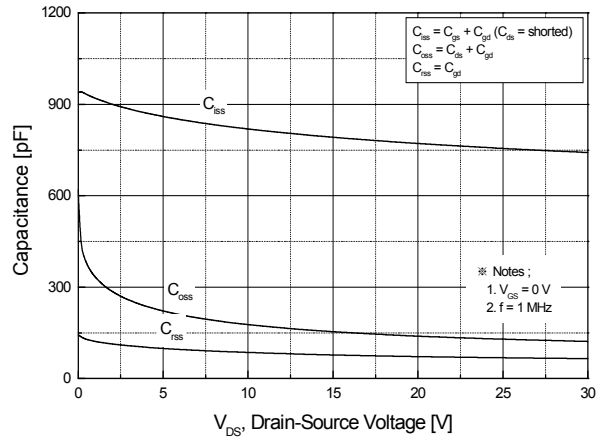


Fig.8 Capacitance Characteristics

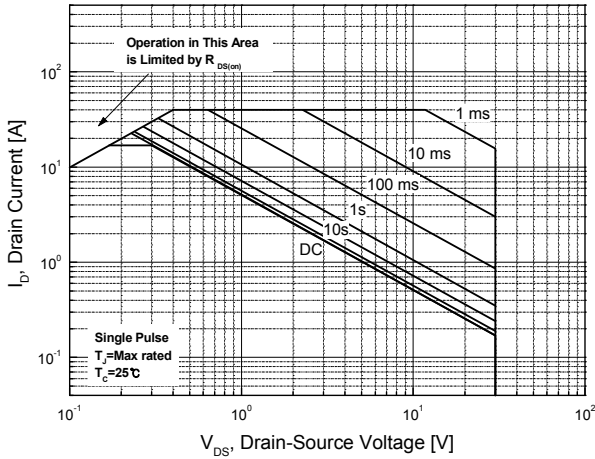


Fig.9 Maximum Safe Operating Area

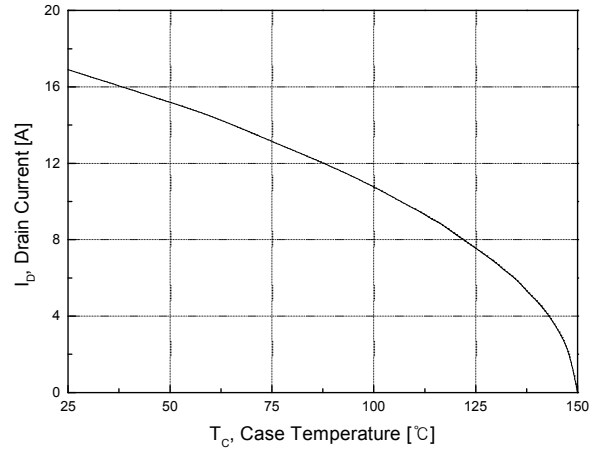


Fig.10 Maximum Drain Current vs. Case Temperature

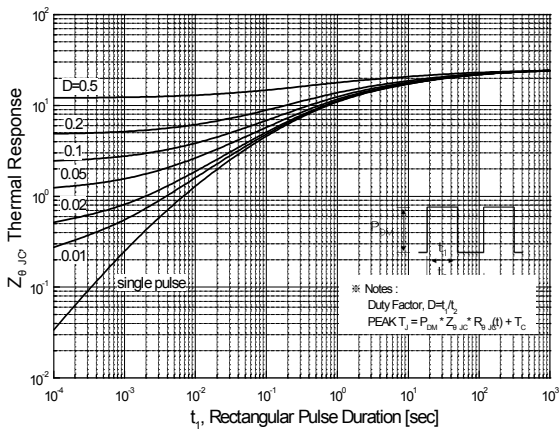
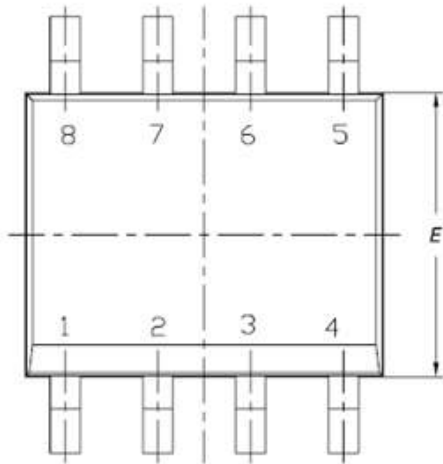


Fig.11 Transient Thermal Response Curve

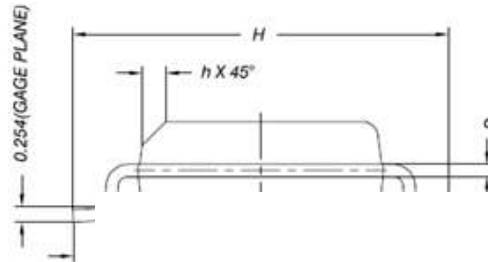
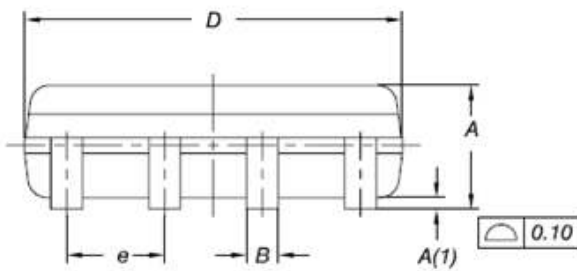
## Physical Dimensions

### 8 Leads, SOIC

Dimensions are in millimeters unless otherwise specified



Symbol	Min	Nom	Max
A	-	-	1.75
A(1)	0.10	-	0.25
B	0.31	-	0.51
C	0.10	-	0.25
D	4.9 BSC		
E	3.9 BSC		
e	1.27 BSC		
H	6.0 BSC		
L	0.40	-	1.27
a	0	-	8
h	0.250	-	0.500
L2(Gage plane)	0.25 BSC		



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