Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2782

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4 V gate drive

• Low drain–source ON resistance $: RDS (ON) = 0.039 \Omega (typ.)$

• High forward transfer admittance $|Y_{fs}| = 11 \text{ S (typ.)}$

• Low leakage current $: I_{DSS} = 100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = 60 \,\text{V})$

• Enhancement-mode : $V_{th} = 0.8 \sim 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	60	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	60	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	20	Α	
	Pulse (Note 1)	I _{DP}	50	Α	
Drain power dissipation (Tc = 25°C)		P_{D}	40	W	
Single pulse avalanche energy (Note 2)		E _{AS}	156	mJ	
Avalanche current		I _{AR}	20	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	4	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

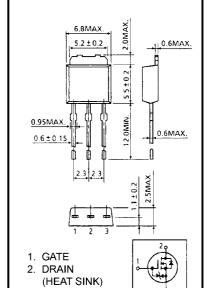
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 530 μ H, R_{G} = 25 Ω , I_{D} = 20 A

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device.

Please handle with caution.



SC-64

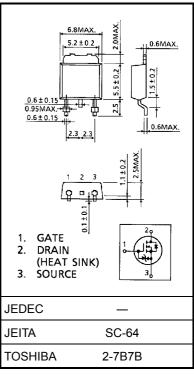
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Weight: 0.36 g (typ.)

3. SOURCE

JEDEC JEITA

TOSHIBA



Weight: 0.36 g (typ.)

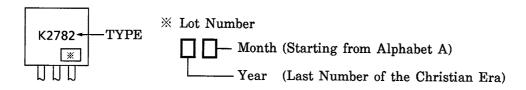
Electrical Characteristics (Ta = 25°C)

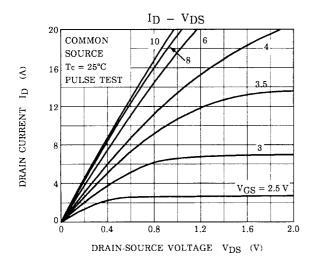
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ	
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_	_	100	μΑ	
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	60	_	_	V	
Gate threshold v	oltage/	V_{th}	V _{DS} = 10 V, I _D = 1 mA	8.0	_	2.0	V	
Drain-source ON resistance		R _{DS (ON)}	V _{DS} = 4 V, I _D = 5 A	_	0.06	0.09	Ω	
			V _{DS} = 10 V, I _D = 10 A	_	0.039	0.055		
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 10 A	7	11	_	S	
Input capacitano	e	C _{iss}		_	880	_		
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	90	_	рF	
Output capacitance		C _{oss}		_	330	_		
Switching time	Rise time	t _r	V _{GS} 10V I _D =10A OV _{OUT}	_	15	_		
	Turn-on time	t _{on}	$R_{L}=3.0\Omega$	_	25	_	ne	
	Fall time	t _f	4, 4, 6	_	30	_	- ns	
	Turn-off time	t _{off}	$V_{DD} = 30V$ Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu s$	_	100	_		
Total gate charg plus gate-drain)		Qg		_	25	_		
Gate-source charge		Q _{gs}	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		19		nC	
Gate-drain ("miller") Charge		Q _{gd}			6			

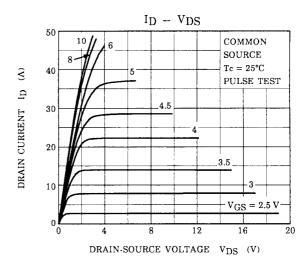
Source-Drain Ratings and Characteristics (Ta = 25°C)

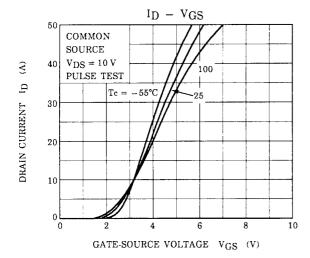
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	20	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	50	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 20 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 20 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs	_	60		ns
Reverse recovery charge	Q _{rr}	1DR - 20 Λ, VGS - 0 V, αιDR / αι - 30 Λ / μS		45	_	μC

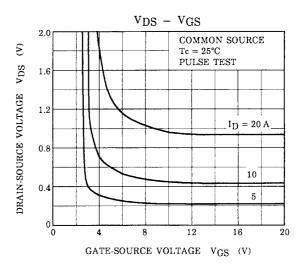
Marking

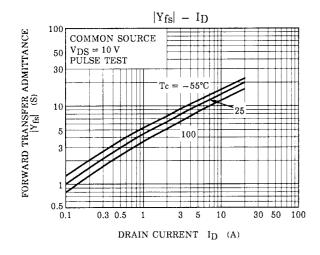


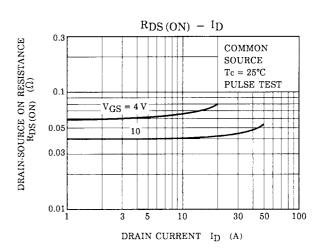




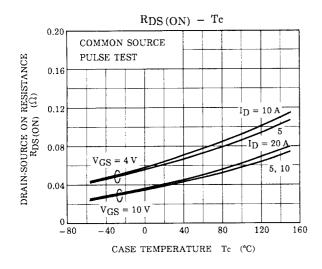


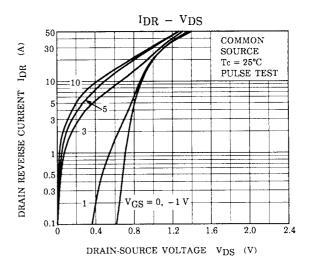


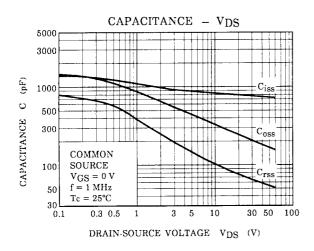


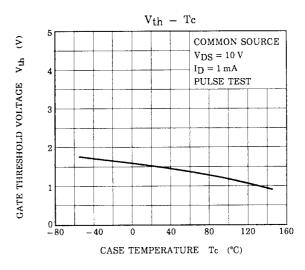


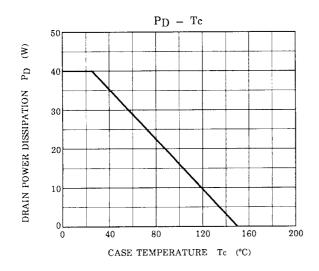
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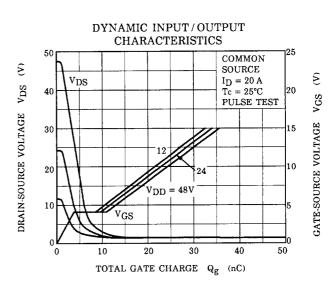




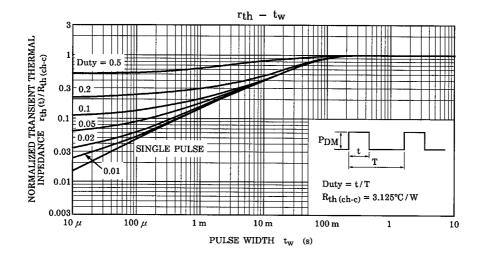


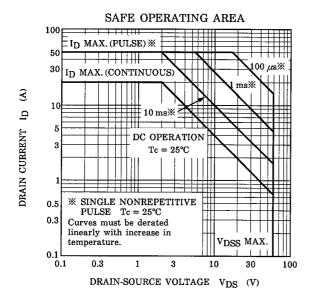


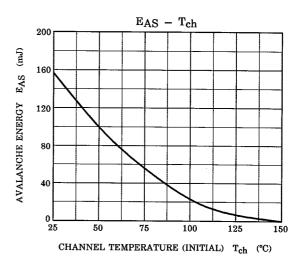


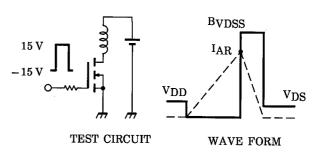


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$$R_G = 25 \Omega$$

 $V_{DD} = 25 V$, $L = 530 \mu H$

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$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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