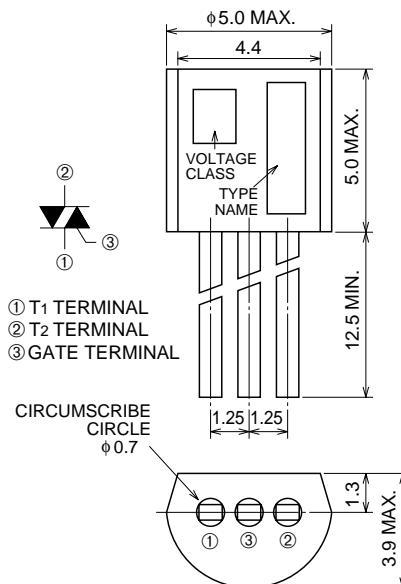


**BCR08AM-14**LOW POWER USE  
PLANAR PASSIVATION TYPE**BCR08AM-14**

- $I_T$  (RMS) ..... 0.8A
- $V_{DRM}$  ..... 700V
- $I_{FGT\ I}, I_{RG\ T\ I}, I_{RG\ T\ III}$  ..... 5mA

**OUTLINE DRAWING**

Dimensions in mm



JEDEC : TO-92

**APPLICATION**

Contactless AC switches, heating, refrigerator, washing machine, electric fan, vending machines, trigger circuit for low and medium triac, solid state relay, other general purpose control applications

**MAXIMUM RATINGS**

Symbol	Parameter	Voltage class	Unit
		14	
$V_{DRM}$	Repetitive peak off-state voltage <sup>*1</sup>	700	V
$V_{DSM}$	Non-repetitive peak off-state voltage <sup>*1</sup>	840	V

Symbol	Parameter	Conditions	Ratings	Unit
$I_T$ (RMS)	RMS on-state current	Commercial frequency, sine full wave 360° conduction, $T_c=67^\circ C$	0.8	A
$I_{TSM}$	Surge on-state current	60Hz sinewave 1 full cycle, peak value, non-repetitive	8	A
$I^2_t$	$I^2_t$ for fusing	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current	0.26	A <sup>2</sup> s
PGM	Peak gate power dissipation		1	W
PG (AV)	Average gate power dissipation		0.1	W
VGM	Peak gate voltage		6	V
IGM	Peak gate current		1	A
$T_j$	Junction temperature		-40 ~ +125	°C
$T_{stg}$	Storage temperature		-40 ~ +125	°C
—	Weight	Typical value	0.23	g

\*1. Gate open.

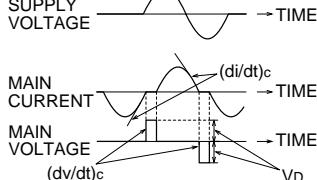
Feb.1999

## ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
I <sub>DRM</sub>	Repetitive peak off-state current	T <sub>j</sub> =125°C, V <sub>DRM</sub> applied	—	—	1.0	mA
V <sub>TM</sub>	On-state voltage	T <sub>c</sub> =25°C, I <sub>TM</sub> =1.2A, Instantaneous measurement	—	—	2.0	V
V <sub>FGT</sub> I	Gate trigger voltage	I	—	—	2.0	V
V <sub>RG</sub> T I		II	—	—	2.0	V
V <sub>RG</sub> T III		III	—	—	2.0	V
I <sub>FGT</sub> I	Gate trigger current	I	—	—	5	mA
I <sub>RG</sub> T I		II	—	—	5	mA
I <sub>RG</sub> T III		III	—	—	5	mA
V <sub>GD</sub>	Gate non-trigger voltage	T <sub>j</sub> =125°C, V <sub>D</sub> =1/2V <sub>DRM</sub>	0.1	—	—	V
R <sub>th</sub> (j-c)	Thermal resistance	Junction to case *3	—	—	50	°C/W
(d <sub>v</sub> /dt) <sub>c</sub>	Critical-rate of rise of off-state commutating voltage		*2	—	—	V/μs

\*2. The critical-rate of rise of the off-state commutating voltage is shown in the table below.

\*3. Case temperature is measured at the T<sub>2</sub> terminal 1.5mm away from the molded case.

Voltage class	V <sub>DRM</sub> (V)	(d <sub>v</sub> /dt) <sub>c</sub>		Test conditions	Commutating voltage and current waveforms (inductive load)
		Min.	Unit		
14	700	0.5	V/μs	1. Junction temperature T <sub>j</sub> =125°C 2. Rate of decay of on-state commutating current (d <sub>i</sub> /dt) <sub>c</sub> =-0.4A/ms 3. Peak off-state voltage V <sub>D</sub> =400V	

## PERFORMANCE CURVES

