

Low-Power Green-Mode PWM Flyback Power Controller without Secondary Feedback
SG6849
FEATURES

- Linearly decreasing PWM frequency
- Green-mode under light-load and zero-load conditions
- Constant voltage (CV) and constant current (CC)
- No secondary feedback
- Low startup current (5uA)
- Low operating current (3.6mA)
- Leading-edge blanking
- Constant power limit
- Universal AC input range
- Synchronized slope compensation
- 150°C OTP sensor with hysteresis
- V_{DD} over voltage clamping
- Cycle-by-cycle current limiting
- Under voltage lockout (UVLO)
- Fixed PWM frequency (65kHz)
- Gate output maximum voltage clamped at 16.7V
- Small SOT-26 package

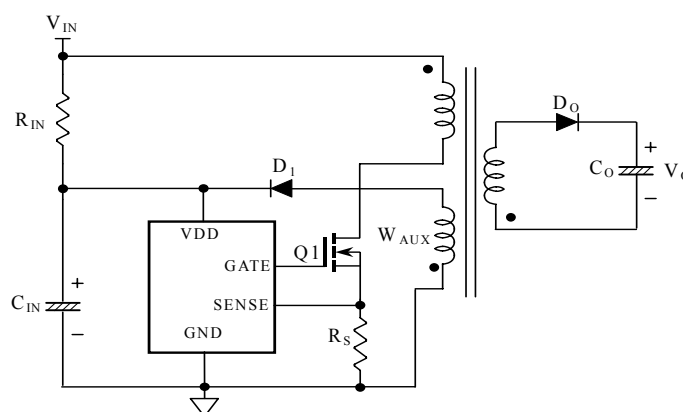
APPLICATIONS

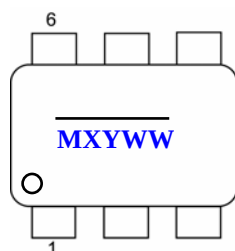
Low-power flyback power converters, such as

- Battery chargers for cellular phones, cordless phones, PDAs, digital cameras, and power tools.
- Power adapters for ink jet printers, video game consoles, and portable audio players.
- Open-frame SMPS for TV/DVD standby and other auxiliary supplies, home appliances, consumer electronics, and PC 5V standby-power.
- Replacements for linear transformers and RCC SMPS.

DESCRIPTION

This highly integrated PWM controller provides several features to enhance the performance of low power flyback converters. To minimize standby power consumption, the proprietary green-mode function provides off-time modulation to linearly decrease the switching frequency under light-load and zero-load conditions. This green-mode function enables the power supply to easily meet international power conservation requirements. The supply voltage V_{DD} is also used for feedback compensation, to regulate the output voltage without requiring a conventional TL431 and a photo-coupler. Another advantage of the SG6849 is that the typical startup current is only 5uA, while the typical operating current can be as low as 3.6mA. A large startup resistance could be used to achieve even higher power conversion efficiency. Built-in synchronized slope compensation maintains the stability of peak current-mode control. Proprietary internal compensation ensures constant output power limiting over a universal range of AC input voltages, from 90VAC to 264VAC. The SG6849 provides many protection functions. Pulse-by-pulse current limiting ensures a constant output current, even if a short circuit occurs. Also, the internal protection circuit will disable PWM output, if V_{DD} exceeds 22.7V. The gate output is clamped at 16.7V to protect the power MOS from over-voltage damage. The built-in over temperature protection (OTP) function will shutdown the controller at 150°C with a 20°C hysteresis. The SG6849 is designed to provide a low-cost total solution for flyback converters. It is available in a small footprint, 6-pin, SOT-26 package.

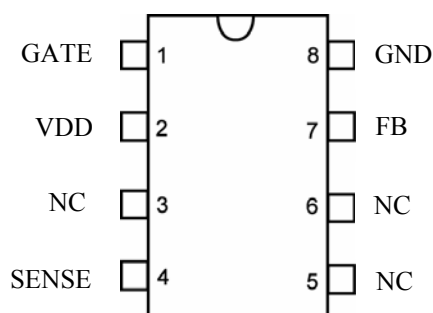
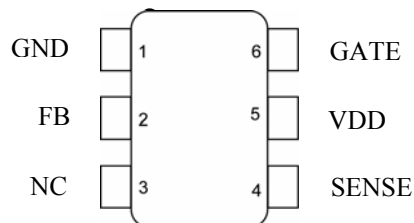
TYPICAL IC APPLICATION


MARKING INFORMATION


M: Mask Version
X: B= SG6849-65T
—: Lead Free
Y: Year
WW: Work Week



ZZZ: 65
D: D=DIP
P: Z =Lead free
 Null=Regular package
M: Mask Version
XXXXXX: Wafer Lot
YY: Year
WW: Week
V: Assembly Location

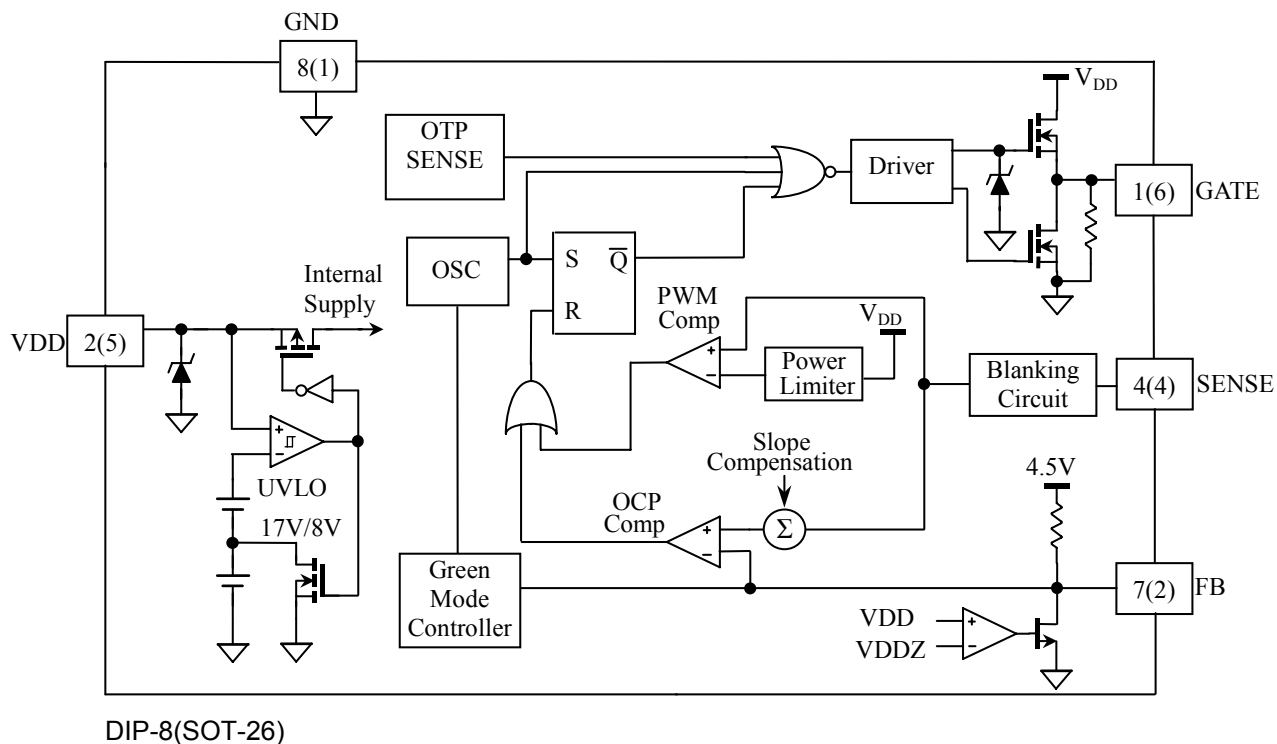
PIN CONFIGURATION

ORDERING INFORMATION

Part Number	PWM Frequency	Package
SG6849-65T	65kHz	6-Pin SOT-26
SG6849-65D	65kHz	8-pin DIP-8
SG6849-65TZ	65kHz	6-Pin SOT-26 (Lead Free)
SG6849-65DZ	65kHz	8-pin DIP-8 (Lead Free)

PIN DESCRIPTIONS

Name	Pin No.		Type	Function
	DIP-8	SOT-26		
GATE	1	6	Driver Output	The totem-pole output driver to drive the power MOSFET.
VDD	2	5	Supply	Power supply.
NC	3	NA		NC pin.
SENSE	4	4	Analog Input	Current sense. It senses the voltage across a sensed resistor. To provide over-current protection, PWM output is disabled if the voltage exceeds an internal threshold. This pin also provides current information for current-mode control.
NC	5	3		NC pin.
NC	6	NA		NC pin.
FB	7	2	Analog Input	Feedback. The FB pin provides feedback information to the internal PWM comparator. This feedback is used to control the duty cycle. When no feedback is provided, this pin is left open.
GND	8	1	Supply	Ground.

BLOCK DIAGRAM



Low-Power Green-Mode PWM Flyback Power Controller without Secondary Feedback
SG6849
ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
V _{VDD}	DC Supply Voltage*	25		V
	Zener Clamp	30		V
	Zener Current	10		mA
V _{FB}	FB Pin Input Voltage	-0.3 to 7		V
V _{SENSE}	Sense Pin Input Voltage	-0.3 to 7		V
P _D	Power Dissipation (T _A =85°C)	SOT-26	247	mW
		DIP-8	478	
R _{θJA}	Thermal Resistance (Junction to Air)**	SOT-26	263.3	°C/W
		DIP-8	135.7	
R _{θJC}	Thermal Resistance (Junction to Case)**	SOT-26	119.6	°C/W
		DIP-8	67.1	
T _J	Operating Junction Temperature	-40 to 125		°C
T _{STG}	Storage Temperature Range	-55 to +150		°C
T _L	Lead Temperature (Wave soldering or IR, 10 seconds)	260		°C
	ESD Capability, Human Body Model	3		KV
	ESD Capability, Machine Model	300		V

* All voltage values, except differential voltages, are given with respect to the network ground terminal.

* Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device.

** Thermal JA test board size: SOT 18×12×1.6mm/FR4; DIP 40×35×1.6mm/FR4

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V _{DD}	DC Supply Voltage	with Secondary Feedback	<20
		without Secondary Feedback	<22.7
T _A	Operating Ambient Temperature	-20 to +85	°C

ELECTRICAL CHARACTERISTICS (V_{DD} = 15V, T_A = 25°C, unless noted)
Feedback Input Section

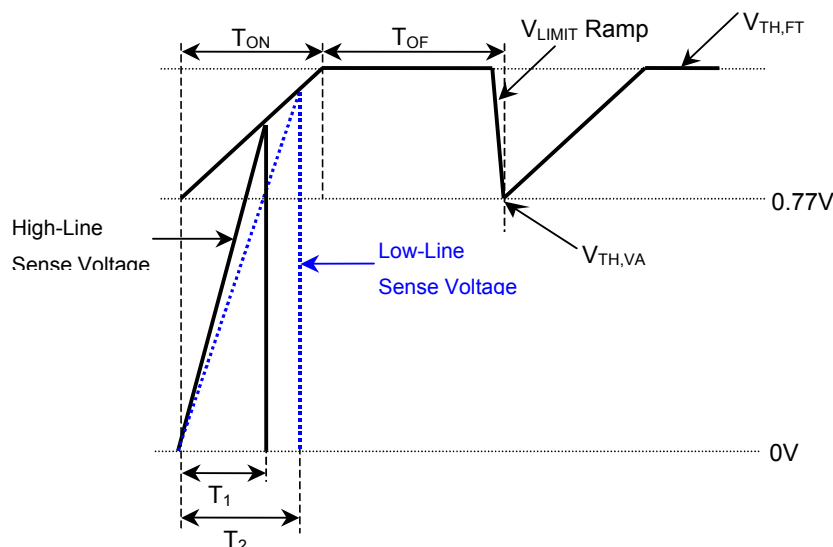
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
I _{FB-ZDC}	Zero Duty-Cycle FB Input Current			1.15	1.4	mA
V _{FB-OPEN}	Open Loop Voltage		4.5			V
Z _{FB}	Input Impedance	I _{FB} =0.1mA to 0.2mA		2.4		KΩ
V _{DD-FB}	V _{DD} Feedback Threshold Voltage*	FB is Open	20	22.7	25	V
		I _{FB} =0.4mA	18	20	22	V

*The feedback input is pulled by a transistor that is controlled by the V_{DD} signal while V_{DD} ≥ V_{DDZ}.

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Current Sense Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
t_{PD}	Propagation Delay			100		ns
V_{STHVA}	Current Limiting Valley Threshold Voltage	$V_{DD}=20V$	0.84	0.89	0.94	V
		$V_{DD}=15V$	0.72	0.77	0.82	V
		$V_{DD}=10V$	0.58	0.63	0.68	V

* For proper operation


Oscillator Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
F_{OSC}	Frequency in nominal mode		60	65	70	KHz
F_{OSC-G}	Green-Mode Frequency		20	25	30	KHz
I_{FB-G}	Green-Mode Ending FB Current		0.9	1.15	1.4	mA
F_{DV}	Frequency Variation versus V_{DD} Deviation	$V_{DD}=10$ to $20V$			2	%
$V_{DD-TH-G}$	V_{DD} -Low Threshold Voltage for Exiting Green-Mode OFF		$V_{DD_{OFF}+1.0}$	$V_{DD_{OFF}+1.5}$	$V_{DD_{OFF}+2.0}$	V

Output Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
DCY_{MAX}	Maximum Duty Cycle		70	75	80	%
t_{LEB}	Leading Edge Blanking Time			310		ns
V_{GATE-L}	Output Voltage Low	$V_{DD}=15V, I_O=10mA$			1.5	V
V_{GATE-H}	Output Voltage High	$V_{DD}=15V, I_O=-10mA$	8			V
t_r	Rising Time	$V_{DD}=15V, C_L=1nF$		200		ns
t_f	Falling Time	$V_{DD}=15V, C_L=1nF$		80		ns
$V_{GATE-CLAMP}$	Output Clamp Voltage	$V_{DD}=20V$		16.7	18	V

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Over Temperature Protection (OTP)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Temp-OTP	Protection Junction Temperature ^{*1}			150		°C
Temp-Restart	Restart Junction Temperature ^{*2}			130		°C

^{*1} When activated, the output is disabled and the latch is turned off.

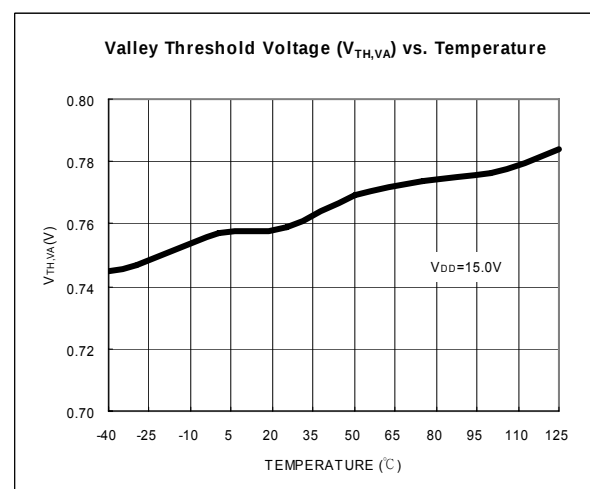
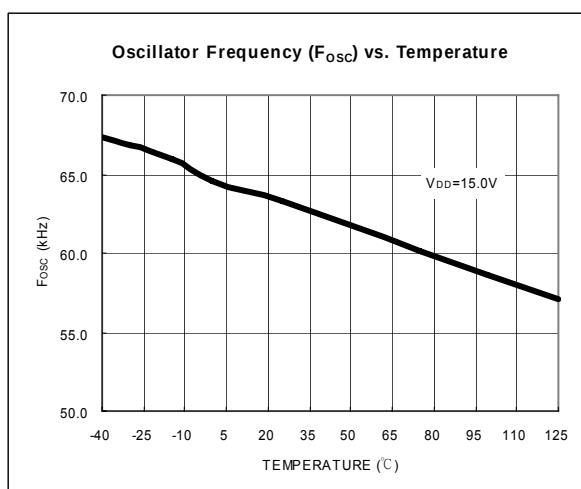
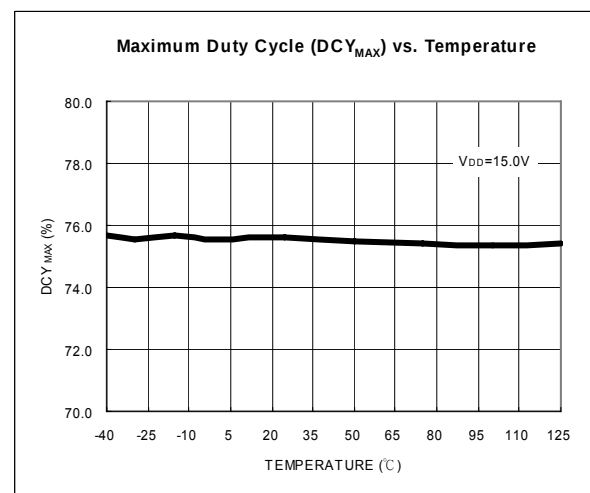
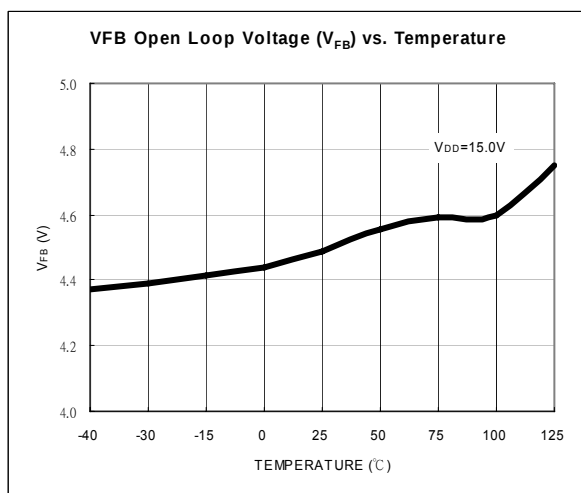
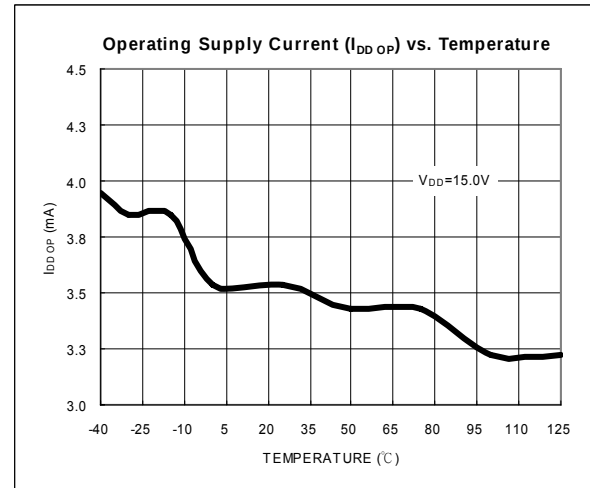
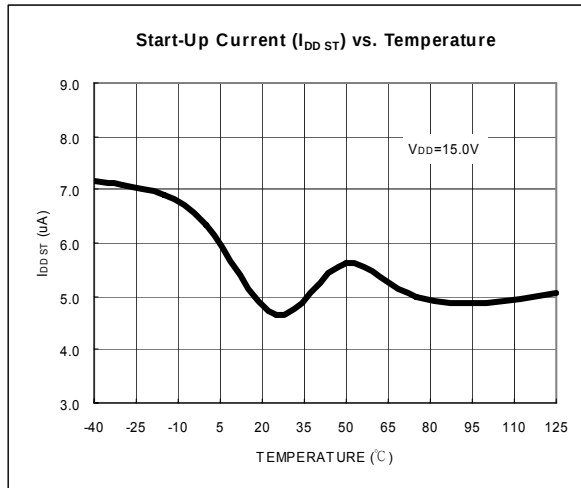
^{*2} This is the threshold temperature for enabling the output again and resetting the latch, after over temperature protection has been activated.

VDD Section

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V _{DD ON}	Turn-On Threshold Voltage		16	17	18	V
V _{DD OFF}	Turn-Off Threshold Voltage		7.5	8	8.5	V
I _{DD ST}	Startup Current	V _{DD} =15V		5	20	uA
I _{DD OP}	Operating Supply Current	V _{DD} =15V C _L =1nF		3.6	4.6	mA
V _{DD CLAMP}	Zener Diode Breakdown Voltage	I _{DD} =10mA		28	30	V

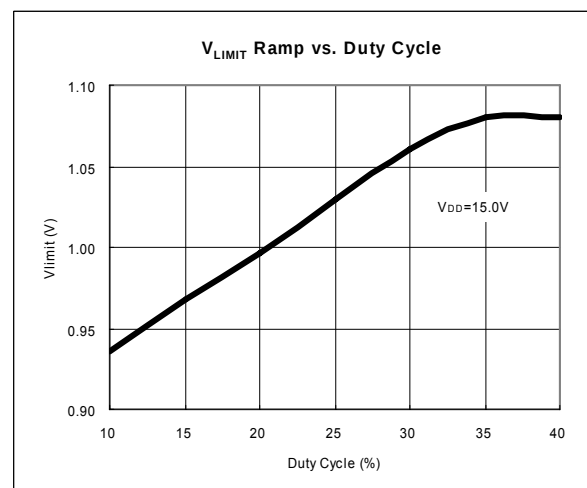
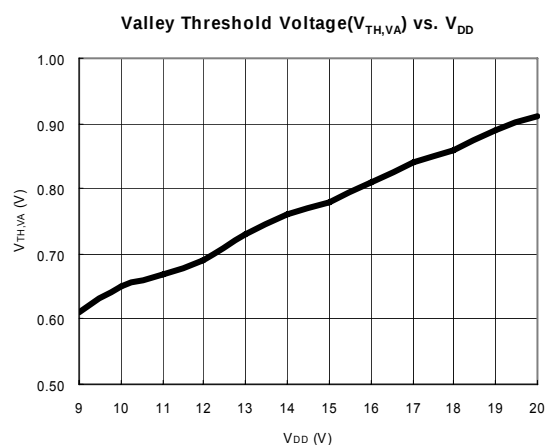
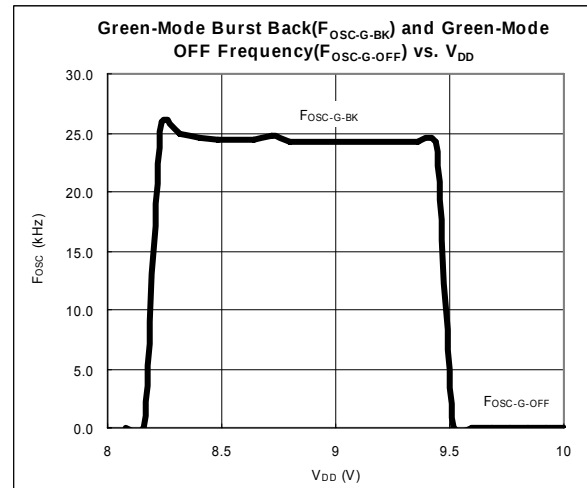
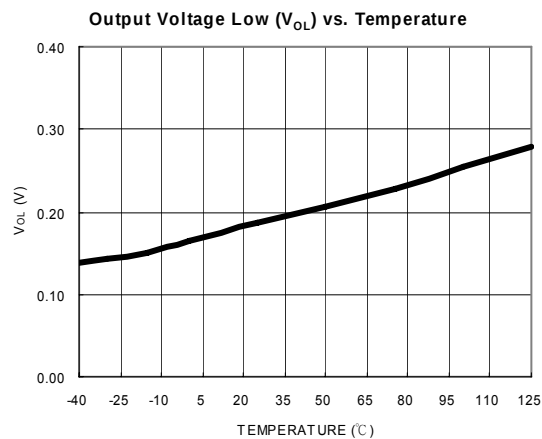
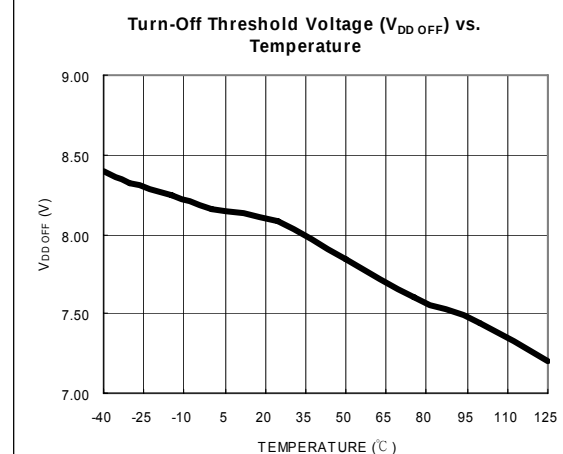
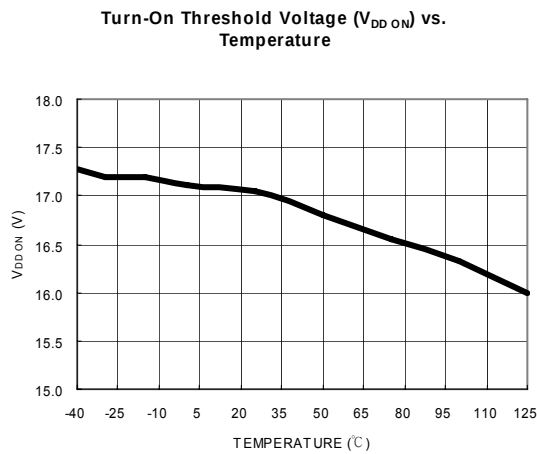
TYPICAL CHARACTERISTICS

SG6849-65



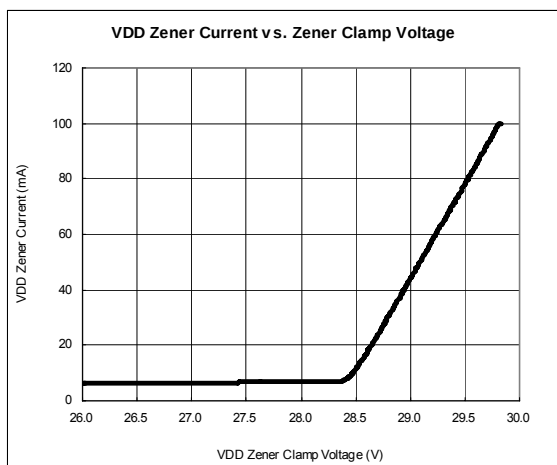
Low-Power Green-Mode PWM Flyback Power Controller without Secondary Feedback

SG6849



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SG6849



OPERATION DESCRIPTION

SG6849 devices integrate many useful functions for low power switch mode power supplies. The following descriptions highlight the key features of the SG6849.

Startup Current

The required startup current is only 5uA. This allows a high-resistance, low-wattage startup resistor to be used to supply the controller's startup power. A 1.5 M Ω /0.25W startup resistor can be used over a wide input range (100V-240V AC) with very little power loss.

Operating Current

The operating current is normally 3.6mA. The low operating current results in higher efficiency and reduces the required V_{DD} hold-up capacitance. A 10uF/25V V_{DD} hold-up capacitor can be used over a wide input range (100V-240V AC) with very little power loss.

Green Mode Operation

The proprietary green-mode function provides off-time modulation to linearly decrease the switching frequency under light-load and zero-load conditions. The on-time is limited to provide better protection against brownouts and other abnormal conditions.

This green-mode function dramatically reduces power consumption under light-load and zero-load conditions. Power supplies using the SG6849 can easily meet international restrictions regarding standby power-consumption.

Constant Voltage (CV) and Constant Current (CC) without Feedback

The SG6849 can tightly regulate the output voltage and provide over current protection without requiring secondary-side feedback signals. For improved CV and CC accuracy, the transformer leakage inductance should be reduced as much as possible.

Over Temperature Protection (OTP)

The SG6849 has a built-in temperature sensing circuit to shut down PWM output once the junction temperature exceeds 150°C. While PWM output is shut down, the V_{DD} voltage will gradually drop to the UVLO voltage. Some of the SG6849's internal circuits will be shut down, and V_{DD} will gradually start increasing again. When V_{DD} reaches 17V, all the internal circuits, including the temperature sensing circuit, will start operating normally. If the junction temperature is still higher than 150°C, the PWM controller will be shut down immediately. This situation will continue until the temperature drops below 130°C. The PWM output will then be turned back on. The temperature hysteresis window for the OTP circuit is 20°C.

V_{DD} Over Voltage Clamping

V_{DD} over voltage clamping is built in to prevent damage from over-voltage conditions. When V_{DD} exceeds 22.7V, PWM output is shut down. Over-voltage conditions may be caused by an open photo-coupler loop or a short circuit in the output.

Oscillator Operation

The oscillation frequency is fixed at 65kHz for the SG6849-65.

Leading Edge Blanking

Each time the power MOSFET is switched on, a turn-on spike will inevitably occur at the sense-resistor. To avoid premature termination of the switching pulse, a 310ns leading-edge blanking time is built in. Conventional RC filtering is not necessary. During this blanking period, the current-limit comparator is disabled, and it cannot switch off the gate drive.

Constant Output Power Limit

When the SENSE voltage across the sense resistor R_s reaches the threshold voltage (around 1.0V), the output GATE drive will be turned off following a small propagation delay T_{PD} . This propagation delay will

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SG6849

introduce an additional current proportional to $T_{PD} \cdot V_{IN} / L_P$. The propagation delay is nearly constant regardless of the input line voltage V_{IN} . Higher input line voltages will result in larger additional currents. Thus, under high input-line voltages the output power limit will be higher than under low input-line voltages.

Over a wide range of AC input voltages, the variation can be significant. To compensate for this, the threshold voltage is adjusted by adding a positive ramp (Vlimit ramp). This ramp signal can vary from 0.77V to 1.05V for SG6849-65, and it flattens out at 1.05V. A smaller threshold voltage forces the output GATE drive to terminate earlier, thus reducing total PWM turn-on time and making the output power equal to that of the low line input. This proprietary internal compensation feature ensures a constant output power limit over a wide range of AC input voltages (90VAC to 264VAC).

Under Voltage Lockout (UVLO)

The turn-on/turn-off thresholds of the SG6849 are fixed internally at 17V/8V. To enable the SG6849 during startup, the hold-up capacitor must first be charged to 17V through the startup resistor.

The hold-up capacitor will continue to supply V_{DD} before energy can be delivered from the auxiliary winding of the main transformer. V_{DD} must not drop below 8V during this startup process. This UVLO hysteresis window ensures that the hold-up capacitor can adequately supply V_{DD} during startup.

Gate Output

The SG6849 BiCMOS output stage is a fast totem pole gate driver. Cross-conduction has been avoided to minimize heat dissipation, increase efficiency, and enhance reliability. The output driver is clamped by an internal 16.7V Zener diode in order to protect the power MOSFET transistors against any harmful over-voltage gate signals.

Slope Compensation

The sensed voltage across the current sense resistor is used for current mode control and pulse-by-pulse current limiting. The built-in slope compensation function improves power supply stability. Furthermore, it prevents sub-harmonic oscillations that normally would occur because of peak current mode control. A positively sloped, synchronized ramp is activated by the SG6849 with every switching cycle. The slope of the ramp is

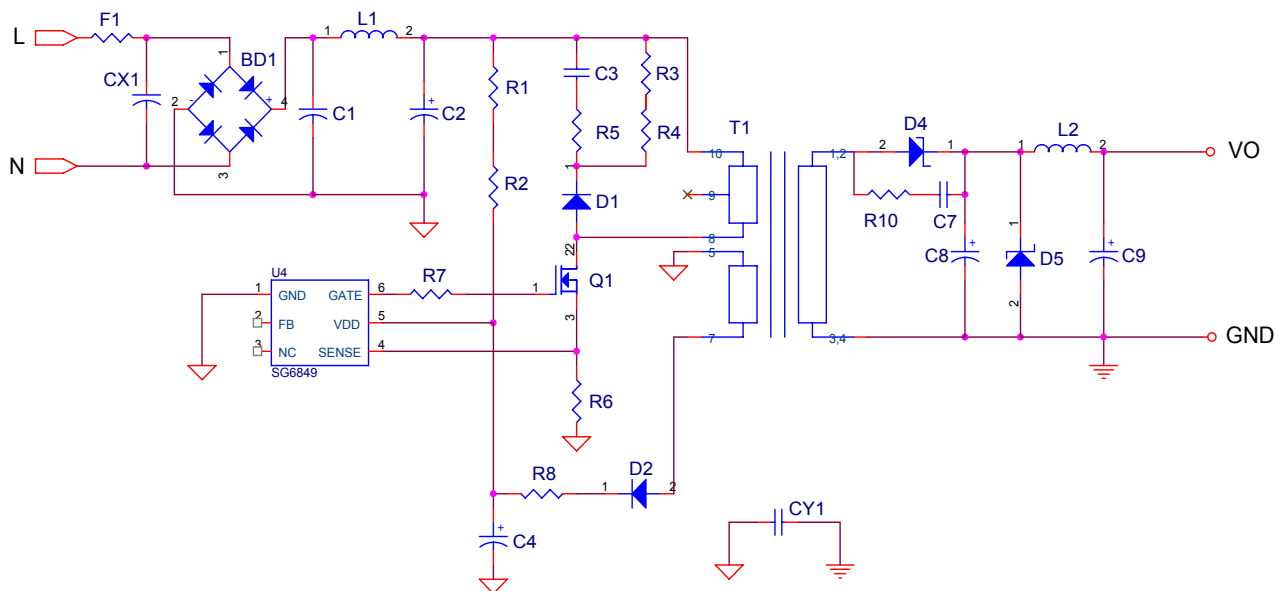
$$\frac{0.33 \times \text{Duty}}{\text{Duty}(\text{max})}$$

Noise Immunity

Noise from the current sense or the control signal may cause significant pulse width jitter, particularly in continuous-conduction mode. Slope compensation helps alleviate this problem. Good placement and layout practices should be followed. The designer should avoid long PCB traces and component leads. Compensation and filter components should be located near the SG6849. Finally, increasing the power-MOS gate resistance is advised.

REFERENCE CIRCUIT

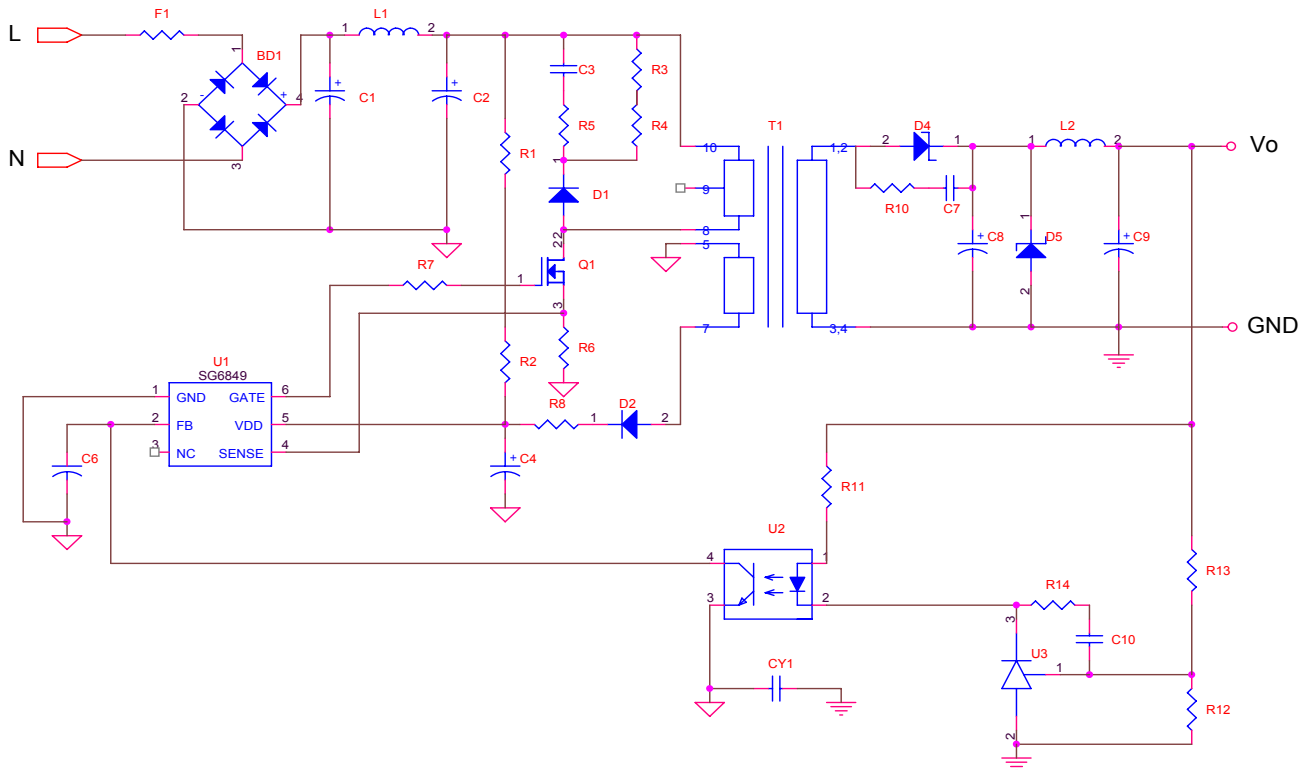
3W Flyback 5V/0.6A Circuit, without Secondary-Side Feedback



BOM

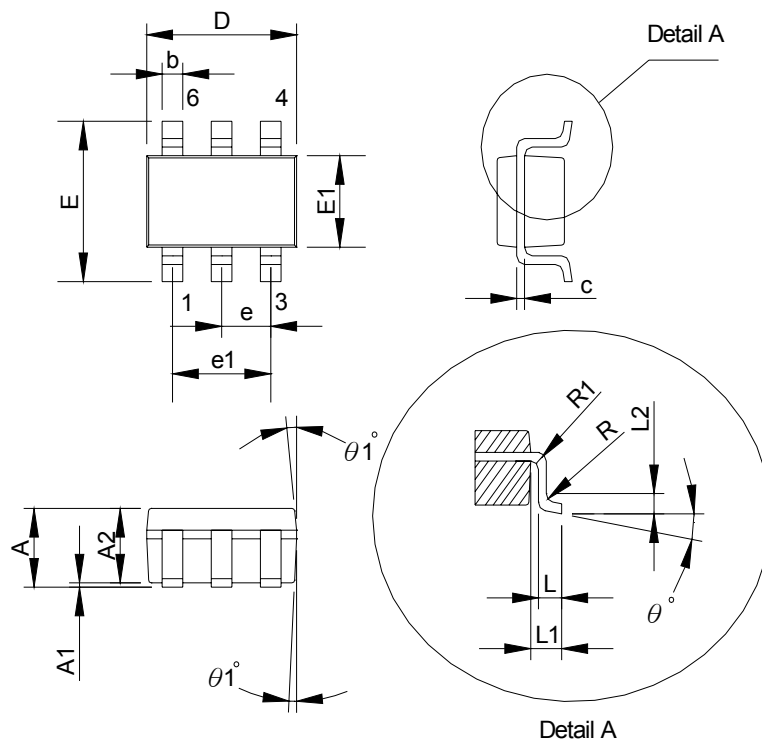
Symbol	Component	Symbol	Component
BD1 (Reference only)	BD DI106 1A/600V	F1	R 1Ω/0.5W
CX1 (Option)	YC 472pF/400V (Y1)	L1	Inductor 20mH 6*8mm
CY1 (Option)	YC 102pF/400V (Y1)	L2	Inductor 10uH 6mm
C1	CC 0.01uF/500V	Q1	MOSFET 1A/600V
C2	EC 10uF/400V 105°C	R1,R2	R 750kΩ/1206
C3	CC 1000pF/500V	R3,R4	R 47kΩ/1206
C4	EC 10uF/50V	R5	R 47Ω/1206
C7 (Option)	CC 102pF/100V 1206	R6	R 4.7Ω/0.5W
C8	EC 470uF/10V 105°C	R7	R 100Ω/0805
C9	EC 220uF/10V 105°C	R8	R 10Ω/1206
D1	Diode FRI07	R10 (Option)	R 10Ω/1206
D2	Diode FR102	T1	Transformer EE-16
D4	Diode SB360	U4	IC SG6849 (Green PWM IC)
D5 (Option)	ZD 6.8V/0.5W		

3W Flyback 5V/0.6A Circuit, with Secondary-Side Feedback

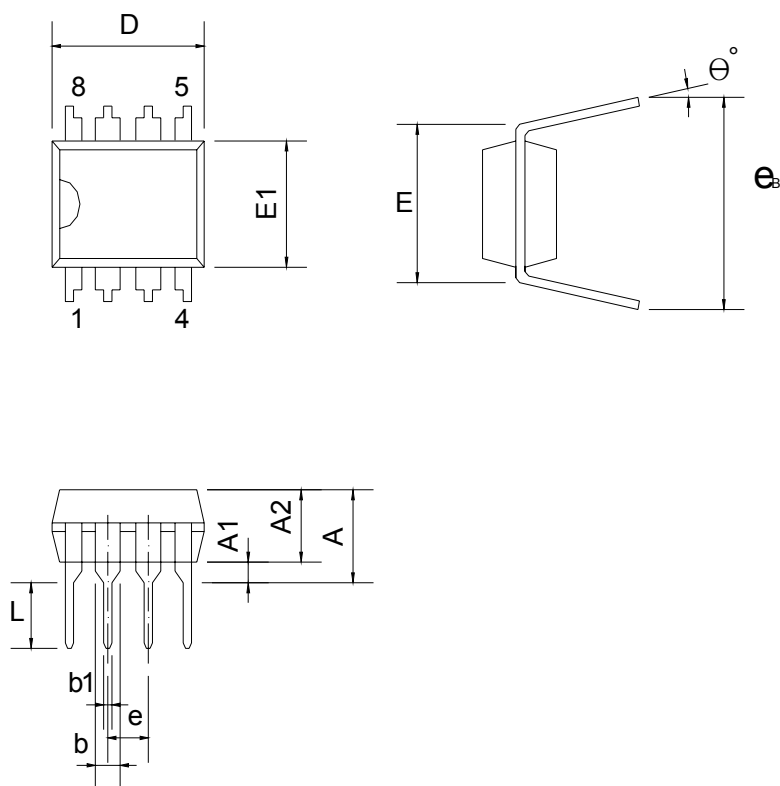


BOM

Symbol	Component	Symbol	Component
BD1 (Reference only)	BD DI106 1A/600V	L2	Inductor 10uH 6mm
CY1 (Option)	YC 102p/400V	Q1	MOSFET 1A/600V
C1	CC 0.01uF/500V	R1,R2	R 750kΩ/1206
C2	EC 10uF/400V 105°C	R4,R3	R 47kΩ/1206
C3	CC 1000p/500V	R5	R 47Ω/1206
C4	EC 10u/50V	R6	R 4.7Ω/0.5W
C6	CC 472p/0805	R7	R 100Ω/0805
C7	CC 102p/100V 1206	R10	R 10Ω/1206
C8	EC 470u/10V 105°C	R8	R 10Ω/1206
C9	EC 470u/10V 105°C	R11	R 100Ω/ 1/8W
C10	CC 222p/0805	R12	R 33kΩ/0805
D1	Diode FR107	R13	R 33kΩ/ 1/8W
D2	Diode FR102	R14	R 4.7kΩ/0805
D4	Diode SB360	T1	Transformer EE-16
D5 (Option)	ZD 6.8V/0.5W	U1	IC SG6849 (Green PWM IC)
F1	R 1Ω/0.5W Resistor	U2	IC PC817
L1	Inductor 20mH 6*8mm	U3	IC TL431

PACKAGE INFORMATION
6 PINS – SOT (T)

Dimensions:

Symbol	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			1.45			0.057
A1			0.15			0.006
A2	0.90	1.15	1.30	0.036	0.045	0.051
b	0.30		0.50	0.011		0.020
c	0.08		0.22	0.003		0.009
D		2.90			0.114	
E		2.80			0.110	
E1		1.60			0.063	
e		0.95			0.037	
e1		1.90			0.075	
L	0.30	0.45	0.60	0.020	0.018	0.24
L1		0.60			0.024	
L2		0.25			0.010	
R	0.10			0.004		
R1	0.10		0.25	0.004		0.25
θ°	0°	4°	8°	0°	4°	8°
θ1°	5°	10°	15°	5°	10°	15°

8 PINS – DIP (D)

Dimensions:

Symbol	Millimeter			Inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			5.334			0.210
A1	0.381			0.015		
A2	3.175	3.302	3.429	0.125	0.130	0.135
b		1.524			0.060	
b1		0.457			0.018	
D	9.017	9.271	10.160	0.355	0.365	0.400
E		7.620			0.300	
E1	6.223	6.350	6.477	0.245	0.250	0.255
e		2.540			0.100	
L	2.921	3.302	3.810	0.115	0.130	0.150
e _B	8.509	9.017	9.525	0.335	0.355	0.375
θ°	0	7	15	0	7	15

DISCLAIMERS

LIFE SUPPORT

System General's products are not designed to be used as components in devices intended to support or sustain human life. Use of System General's products in components intended for surgical implant into the body, or other applications in which the failure of System General's products could create a situation where personal death or injury may occur, is not authorized without the express written approval of System General's Chief Executive Officer. System General will not be held liable for any damages or claims resulting from the use of its products in medical applications.

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